

1 Amp Small Signal Diode

DO-35

FEATURE:

ABSOLUTE MAXIMUM RATINGS¹ (Ta = 25°C Unless otherwise specified)

1. This product is available in AEC-Q101 Compliant and PPAP Capable also.

PARAMETER	SYMBOL	VALUE	UNIT	
Maximum Repetitive Reverse Voltage	V _{RRM}	200	V	
Average Rectified Forward Current	I _{F(AV)}	100	mA	
Non-repetitive Peak Forward Surge Current Pulse Width = 1.0 second		1.0	А	
Non-repetitive Peak Forward Surge Current Pulse Width = 1.0 μs	I _{FSM}	4.0	А	
Storage Temperature Range	T _{STG}	-65 to +200	°C	
Operating Junction Temperature	TJ	-65 to +175	°C	

Note: For AEC-Q101 compliant products, please use suffix -AQ in the part number while ordering.

THERMAL RESISTANCE

Power Dissipation	P _D	500	mW
Thermal Resistance, Junction to Ambient	$R_{ ext{ ext{ heta}JA}}$	300	°C

ELECTRICAL CHARACTERISTICS at (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Breakdown Voltage	V _R	I _R = 100μΑ	200			V
Forward Voltage	V _F	I _F = 100mA			1.0	V
Reverse Leakage	1	V _R = 175V			100	nA
Reverse Leakage	I _R	V _R = 175V, T _A = 150°C			100	μA
Total Capacitance	C _T	V _R = 0V, f = 1MHz			5	pF
Reverse Recovery Time	+	$t_{rr} \qquad I_F = 3mA, I_R = 30mA$ $I_{rr} = 1mA, R_L = 100\Omega$			50	nc
Reverse Recovery Time	۲r				50	ns

Note:

1. These ratings are limiting values above which the serviceability of the diode may be impaired.

2. These ratings are based on a maximum junction temperature of 200°C

3. These are steady limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

1N4938 Rev05_ 14022024E

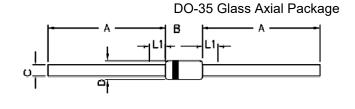
1N4938

DO-35 Axial Leaded Glass Package RoHS compliant





PACKAGE DETAILS



All Dimensions are in mm

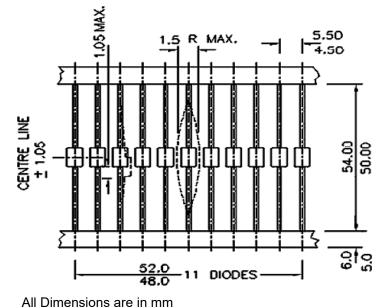
DIM	MIN	MAX
Α	25.40	38.10
В	3.05	5.08
С		0.55
D	1.53	2.28
L1		1.27

Note:

Cathode is Marked by Band



DO-35, 52mm Taping Specification



52mm Taping Specification

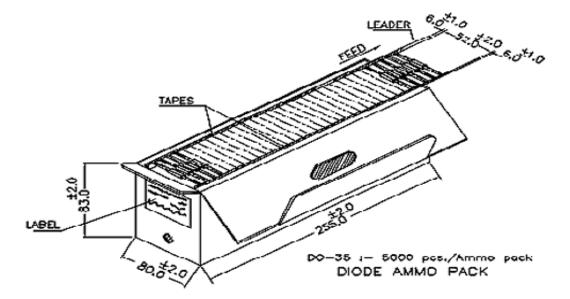
1. T & A Indicates Axial Tape & Ammo Packing (52 mm Tape Spacing)

2. 300 mm(min) leader tape on everyspool

3. No. of empty places allowed 0.25% without Consecutive empty places

4. Ends of leads shall preferably not protrude beyond the tapes

5. Components shall be help sufficiently in the tape or tapes so that they can not come free in normal



on request also available in 26 mm Tape and Ammo Pack

Packing Detail

PACKAGE	STANDARI	DPACK	K INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Qly
DO-35 T&A	5K/ammo box	0.88kg/5K pcs	10"X3.5"X3.5"	5.0K	12.7"X12.7"X20"	125.0K	25Kgs

1N4938 Rev05_ 14022024E

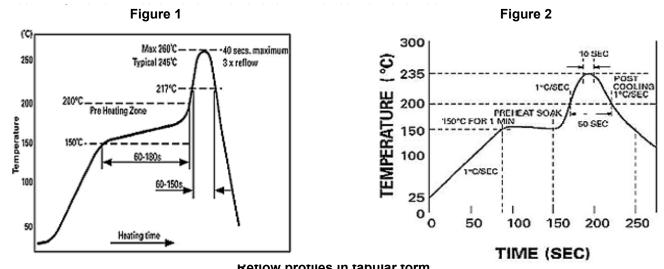




Recommended Reflow Solder Profiles

The recommended reflow solder profiles for Pb and Pb-free devices are shown below.

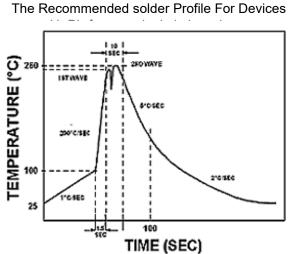
Figure 1 shows the recommended solder profile for devices that have Pb-free terminal plating, and where a Pb-Figure 2 shows the recommended solder profile for devices with Pb-free terminal plating used with leaded

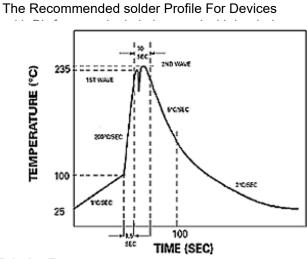


Renow profiles in tabular form				
Profile Feature	Sn-Pb System	Pb-Free System		
Average Ramp-Up Rate	~3°C/second	~3°C/second		
Preheat				
Time maintained above:				
Peak Temperature	235°C	260°C max.		
Time within +0 -5°C of actual Peak	10 seconds	40 seconds		
Ramp-Down Rate	3°C/second max.	6°C/second max.		









Wave Profiles in Tabular Form

Profile Feature	Sn-Pb System	Pb-Free System		
Average Ramp-Up Rate	~200°C/second	~200°C/second		
Heating rate during preheat	Typical 1-2, Max 4°C/sec	Typical 1-2, Max 4°C/Sec		
Final preheat Temperature	Within 125°C of Solder Temp	Within 125°C of Solder Temp		
Peak Temperature	235°C	260°C max.		
Time within +0 -5°C of actual Peak	10 seconds	10 seconds		
Ramp-Down Rate	5°C/second max.	5°C/second max		

Recommended Wave Solder Profiles





Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- \cdot Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤30 °C / 85% RH
2	1 Year	≤30 °C / 60% RH
2a	4 Weeks	≤30 °C / 60% RH
3	168 Hours	≤30 °C / 60% RH
4	72 Hours	≤30 °C / 60% RH
5	48 Hours	≤30 °C / 60% RH
5a	24 Hours	≤30 °C / 60% RH
6	Time on Label(TOL)	≤30 °C / 60% RH





Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered trademark of **Continental Device India Pvt. Limited** C-120 Naraina Industrial Area, New Delhi 110 028, India. Telephone +91-11-2579 6150, 4141 1112 Fax +91-11-2579 5290, 4141 1119 email@cdil.com www.cdil.com CIN No. U32109DL1964PTC004291