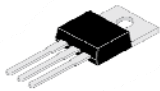


NPN PLASTIC POWER TRANSISTORS

2N6292



TO-220

TO-220
Leaded Plastic
Package
RoHS compliant

COMPLEMENTARY - 2N6107 (PNP)

FEATURES:

1. This product is available in AEC-Q101 Compliant and PPAP Capable also.

Note: For AEC-Q101 compliant products, please use suffix -AQ in the part number while ordering.

APPLICATIONS: General Purpose Amplifier and Switching Applications

ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

PARAMETER		SYMBOL	VALUE	UNIT
Collector Base Voltage		V_{CBO}	80	V
Collector Emitter Voltage		V_{CEO}	70	V
Collector Emitter Voltage ($R_{BE} = 100\Omega$)		V_{CER}	80	V
Emitter Base Voltage		V_{EBO}	5	V
Collector Current (Peak)		I_{CM}	10	A
Collector Current Continuous		I_C	7	A
Base Current		I_B	3	A
Power Dissipation	$T_c = 25^\circ\text{C}$	P_D	40	W
	Derating factor above 25°C		0.32	W/ $^\circ\text{C}$
Power Dissipation	$T_a = 25^\circ\text{C}$	P_D	2	W
	Derating factor above 25°C		16	mW/ $^\circ\text{C}$
Junction Temperature		T_j	150	$^\circ\text{C}$
Storage Temperature		T_{stg}	- 65 to 150	$^\circ\text{C}$
THERMAL RESISTANCE				
Junction to Case		$R_{\theta(j-c)}$	3.125	$^\circ\text{C}/\text{W}$
Junction to Ambient		$R_{\theta(j-a)}$	62.5	$^\circ\text{C}/\text{W}$

2N6292

Rev2_01072022EM



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ELECTRICAL CHARACTERISTICS (T_a=25°C unless specified otherwise)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	MAX	UNIT	
Collector Cut off Current	I _{CEO}	V _{CE} =60V, I _B =0	-	1	mA	
Collector Cut off Current	I _{CEX}	V _{EB(off)} =1.5V; V _{CE} =80V	-	0.1	mA	
Collector Cut off Current		V _{EB(off)} =1.5V; V _{CE} =70V; T _c =150°C	-	2	mA	
Emitter Cut off Current	I _{EBO}	V _{EB} =5V, I _C =0	-	1	mA	
Collector Emitter (sus) Voltage	*V _{CEO(sus)}	I _C =100mA, I _B =0	70	-	V	
Collector Emitter Saturation	*V _{CE(sat)}	I _C =3A, I _B =0.3A	-	1	V	
Collector Emitter Saturation		I _C =7A, I _B =3.0A	-	3.5	V	
Base Emitter on Voltage	*V _{BE(on)}	I _C =2A, V _{CE} =4V	-	1.5	V	
Base Emitter on Voltage		I _C =7A, V _{CE} =4V	-	3	V	
DC Current Gain	*h _{FE}	I _C =2A, V _{CE} =4V	30	150		
DC Current Gain		I _C =7A, V _{CE} =4V	2.3			
DYNAMIC CHARACTERISTIC						
Small Signal Current Gain	h _{fe}	I _C =500mA, V _{CE} =4V, f	20	-		
Output Capacitance	C _{ob}	I _E =0, V _{CB} =10V, f =1 MHz	-	250	pF	
Transition frequency	2N6107	f _T	I _C =500mA, V _{CE} =4V	10	-	MHz
	2N6292			4	-	MHz

*Pulse Test : Pulse duration ≤300μs; Duty cycle ≤1.5%

Recommended Reflow Solder Profiles

The recommended reflow solder profiles for Pb and Pb-free devices are shown below.

Figure 1 shows the recommended solder profile for devices that have Pb-free terminal plating, and where a Pb-free solder is used.

Figure 2 shows the recommended solder profile for devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with a leaded solder.

Figure 1

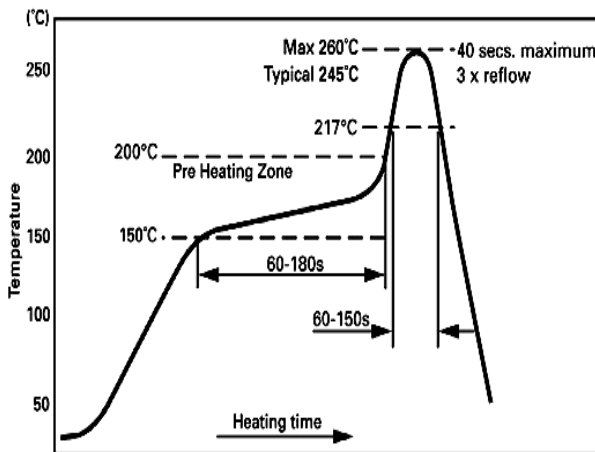
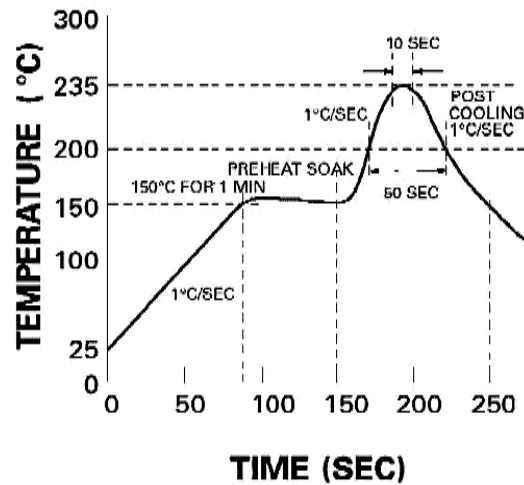


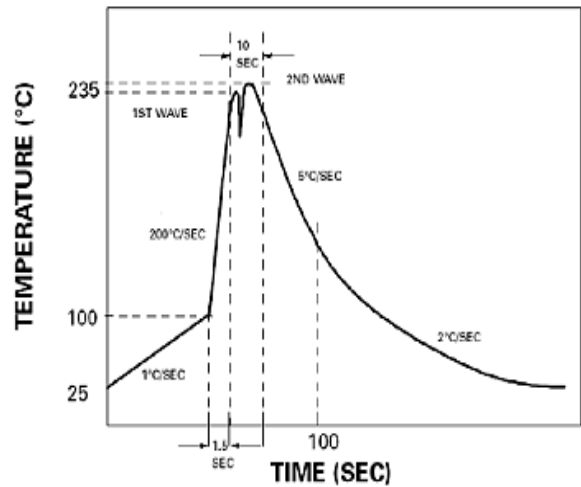
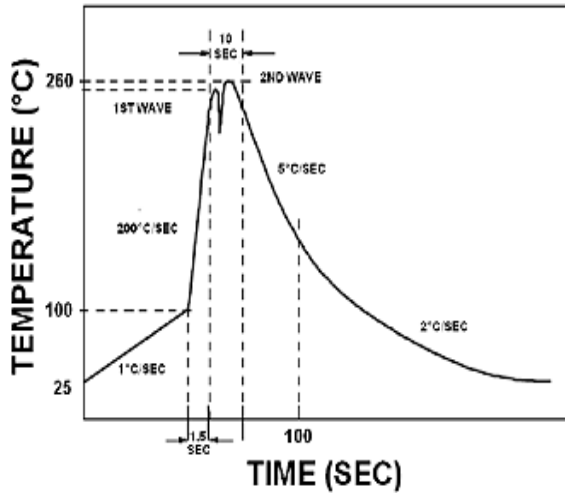
Figure 2



Reflow profiles in tabular form		
Profile Feature	Sn-Pb System	Pb-Free System
Average Ramp-Up Rate	~3°C/second	~3°C/second
Preheat		
– Temperature Range	150-170°C	150-200°C
– Time	60-180 seconds	60-180 seconds
Time maintained above:		
– Temperature	200°C	217°C
– Time	30-50 seconds	60-150 seconds
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	40 seconds
Ramp-Down Rate	3°C/second max.	6°C/second max.

Recommended Wave Solder Profiles

<p>The Recommended solder Profile For Devices with Pb-free terminal plating where a Pb-free solder is used</p>	<p>The Recommended solder Profile For Devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with leaded solder</p>
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Wave Profiles in Tabular Form		
Profile Feature	Sn-Pb System	Pb-Free System
Average Ramp-Up Rate	~200°C/second	~200°C/second
Heating rate during preheat	Typical 1-2, Max 4°C/sec	Typical 1-2, Max 4°C/Sec
Final preheat Temperature	Within 125°C of Solder Temp	Within 125°C of Solder Temp
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	10 seconds
Ramp-Down Rate	5°C/second max.	5°C/second max

TYPICAL CHARACTERISTICS CURVE

Figure 1.

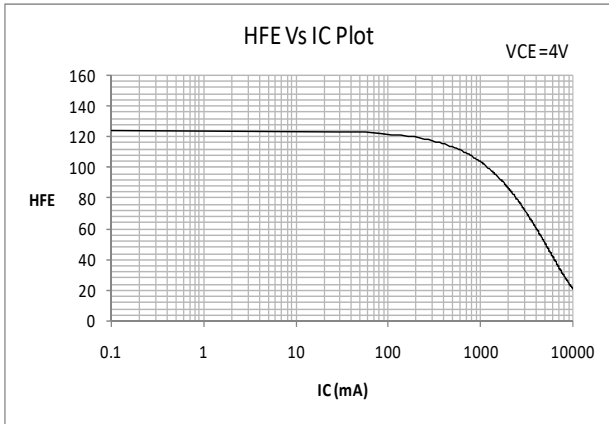


Figure 3.

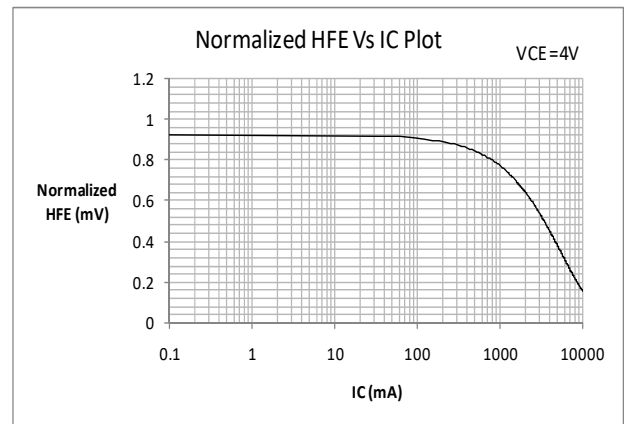


Figure 2.

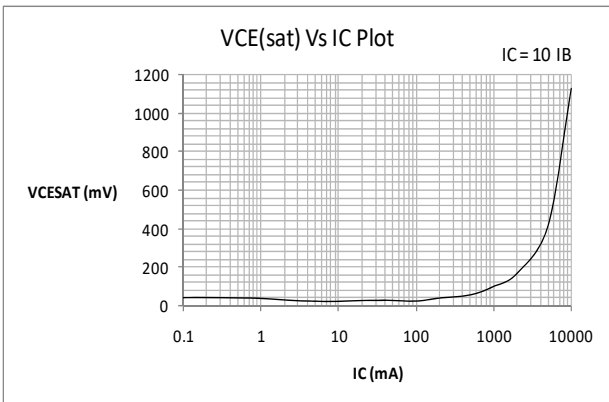


Figure 4.

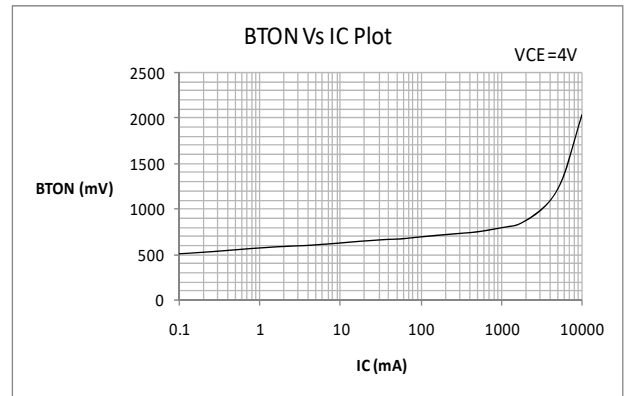
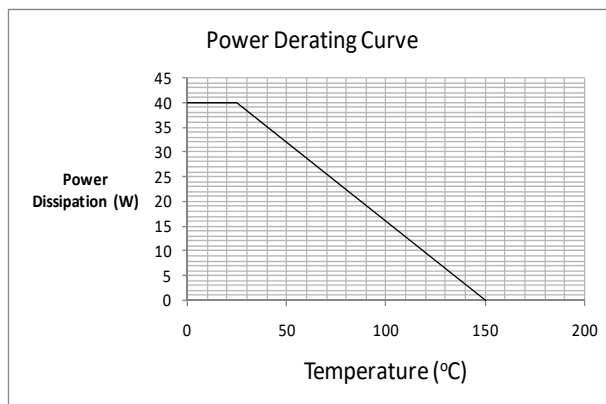
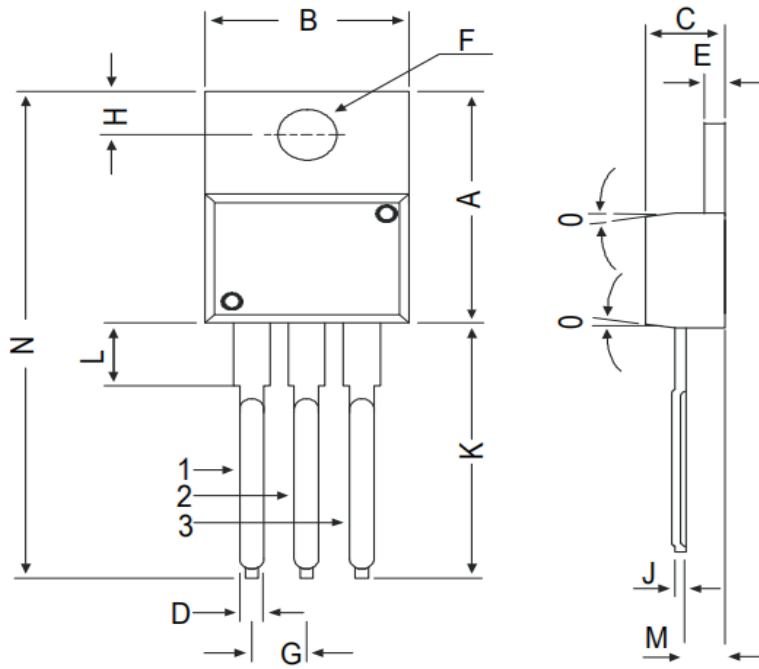


Figure 5.

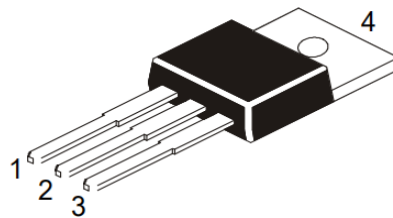


PACKAGE DETAILS

TO-220 Plastic Package



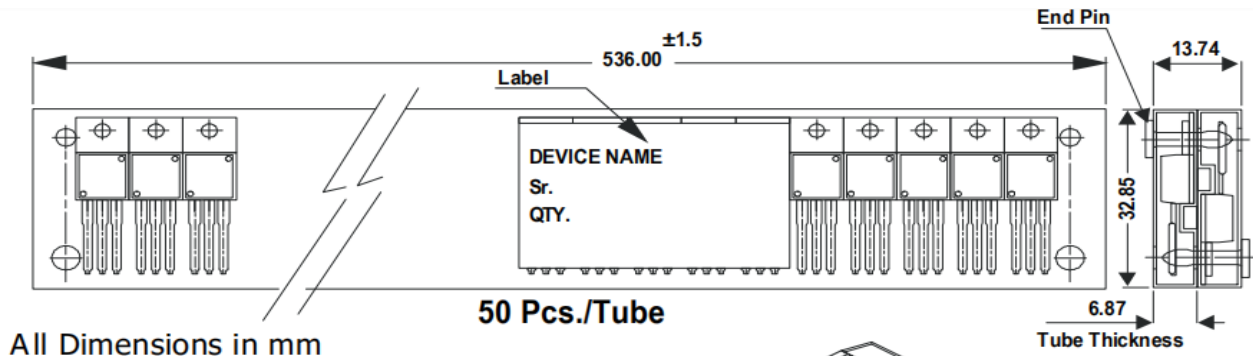
DIM	MIN	MAX
A	14.42	16.51
B	9.63	10.67
C	3.56	4.83
D	—	0.90
E	1.15	1.50
F	3.53	4.10
G	2.29	2.79
H	2.54	3.43
J	0.36	0.61
K	12.70	14.73
L	2.80	6.35
M	2.00	2.92
N	—	31.24
O	7 DEG Acti	



Pin Configuration

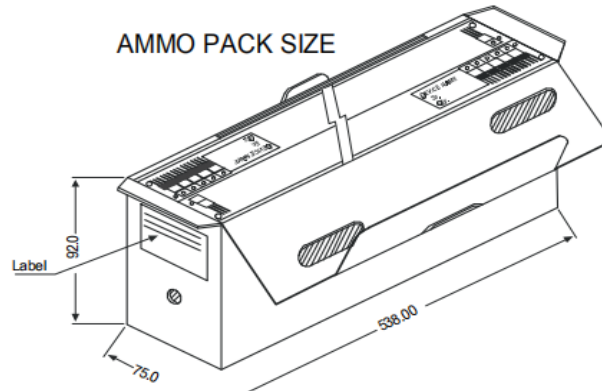
1. Base
2. Collector
3. Emitter
4. Collector

TO-220 Tube Packing



All Dimensions in mm

AMMO PACK SIZE



Activate Win

T & A: Tape and Ammo Pack; T & R: Tape and Reel; Bulk: Loose in Poly Bags; Tube: Tube and Carton; K: 1,000

Package / Case Type	Packaging Type	Std. Packing		Inner Carton		Outer Carton		
		Qty	Qty	Size L x W x H (cm)	Gross Weight (Kg)	Qty	Size L x W x H (cm)	Gross Weight (Kg)
TO-220	Bulk	1,000	1K	19 x 19 x 8	2.0	10K	46 x 38 x 22	21.6
	Tube	1,000 (50 pcs/tube)	1K	55 x 8 x 10	2.8	10K	55 x 35 x 27	28.3



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Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤30 °C / 85% RH
2	1 Year	≤30 °C / 60% RH
2a	4 Weeks	≤30 °C / 60% RH
3	168 Hours	≤30 °C / 60% RH
4	72 Hours	≤30 °C / 60% RH
5	48 Hours	≤30 °C / 60% RH
5a	24 Hours	≤30 °C / 60% RH
6	Time on Label(TOL)	≤30 °C / 60% RH

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Customer Notes

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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