

An IATF 16949, ISO9001 and ISO 14001 Certified Company





Transient Voltage Suppressors



30KPAXX

R-6 Leaded Plastic Package RoHS compliant

FEATURES:

- 1. Glass passivated chip
- 2. 30000 W peak pulse power capability with a 10/1000 µs waveform, repetitive rate (duty cycle):0.01 %
- 3. Low leakage
- 4. Uni and Bidirectional unit
- 5. Excellent clamping capability
- 6. Very fast response time

ABSOLUTE MAXIMUM RATINGS ($T_a = 25$ °C)

PARAMETER	SYMBOL	VALUE	UNIT
Peak power dissipation with a 10/1000 µs waveform(1)	P_{PP}	30000	W
Peak pulse current wih a 10/1000 µs waveform(1)	I _{PP}	See Next Table	Α
Power dissipation on infinite heatsink at T _L = 75 °C	P_{D}	8	W
Peak forward surge current, 8.3 ms single half sinewave unidirectional only(2)	I _{FSM}	500	А
Operating junction and storage temperature range	T_J, T_{STG}	-55 to 150	°C

Note:

- (1) Non-repetitive current pulse per Fig.5 and derated above T_A= 25 °C per Fig.1
- (2) Measured on 8.3 ms single half sine-wave or equivalent square wave, duty cycle = 4 pulses per minute maximum







ELECTRICAL CHARACTERISTICS (T_A =25 °C unless otherwise noted)

Part No. (Uni)	Part Number (B _i)	Breakdown Voltage V _{BR} @ I _T			Maximum Reverse Leakage I _R @V _{RWM}	Working Peak Reverse Voltage V _{RWM}	Maximum Reverse Surge Current I _{PP}	Maximum Clamping Voltage V _C @I _{PP}
		Min (V)	IT (mA)	Max (V)	(uA)	(V)	(A)	(V)
30KPA28A	30KPA28CA	31.28	50	34.58	5000	28	606	50
30KPA30A	30KPA30CA	33.51	50	37.05	5000	30	548.9	55.2
30KPA33A	30KPA33CA	36.9	50	40.76	5000	33	517.9	58.5
30KPA36A	30KPA36CA	40.2	50	44.47	5000	36	490.3	61.8
30KPA39A	30KPA39CA	43.6	20	48.17	2000	39	450.9	67.2
30KPA42A	30KPA42CA	46.9	10	51.88	1000	42	420.8	72
30KPA43A	30KPA43CA	48	10	53.11	1000	43	415.1	73
30KPA45A	30KPA45CA	50.3	5	55.58	250	45	391.5	77.4
30KPA48A	30KPA48CA	53.6	5	59.29	150	48	371.3	81.6
30KPA51A	30KPA51CA	57	5	63	50	51	350.7	86.4
30KPA54A	30KPA54CA	60.3	5	66.70	20	54	331.5	91.4
30KPA58A	30KPA58CA	64.8	5	71.64	20	58	327.9	92.4
30KPA60A	30KPA60CA	67	5	74.11	15	60	297.1	102
30KPA64A	30KPA64CA	71.5	5	79.05	10	64	291.3	104
30KPA66A	30KPA66CA	73.7	5	81.52	2	66	283.2	107
30KPA70A	30KPA70CA	78.2	5	86.47	2	70	278	109
30KPA71A	30KPA71CA	79.3	5	87.7	2	71	271.7	111.5
30KPA72A	30KPA72CA	80.4	5	88.94	2	72	265.8	114
30KPA75A	30KPA75CA	83.8	5	92.64	2	75	253.8	119.4
30KPA78A	30KPA78CA	87.1	5	96.35	2	78	234.9	129
30KPA84A	30KPA84CA	93.8	5	103.76	2	84	217.7	139.2
30KPA90A	30KPA90CA	100.5	5	111.17	2	90	207	146.4
30KPA96A	30KPA96CA	107.2	5	118.58	2	96	194.2	156
30KPA102A	30KPA102CA	113.9	5	126	2	102	183	165.6
30KPA108A	30KPA108CA	120.6	5	133.41	2	108	172.9	175.2
30KPA120A	30KPA120CA	134	5	148.23	2	120	155.9	194.4
30KPA132A	30KPA132CA	147.4	5	163.05	2	132	142.3	213
30KPA144A	30KPA144CA	160.8	5	128.52	2	144	135.8	223.2
30KPA150A	30KPA150CA	167.6	5	185.29	2	150	129.8	233.4
30KPA156A	30KPA156CA	174.3	5	192.70	2	156	123.7	245
30KPA160A	30KPA160CA	178.7	5	197.64	2	160	120	252.6

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ELECTRICAL CHARACTERISTICS (T_A =25 °C unless otherwise noted)

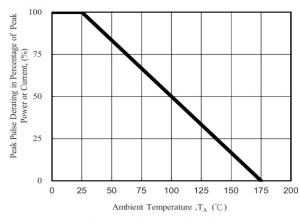
Part No. (Uni)	Part Number (B _i)	Breakdown Voltage V _{BR} @ I _T			Maximum Reverse Leakage I _R @V _{RWM}	Working Peak Reverse Voltage V _{RWM}	Maximum Reverse Surge Current I _{PP}	Maximum Clamping Voltage V _C @I _{PP}
		Min (V)	I _T (mA)	Max (V)	(uA)	(V)	(A)	(V)
30KPA168A	30KPA168CA	187.7	5	207.52	2	168	111.2	272.4
30KPA170A	30KPA170CA	189.9	5	210	2	170	110.2	275
30KPA180A	30KPA180CA	201.1	5	222.35	2	180	104.3	290.4
30KPA198A	30KPA198CA	221.2	5	244.58	2	198	94.7	319.8
30KPA216A	30KPA216CA	241.3	5	266.82	2	216	86.9	348.6
30KPA240A	30KPA240CA	268.1	5	296.47	2	240	78.3	387
30KPA258A	30KPA258CA	288.2	5	318.70	2	258	72.8	416.4
30KPA260A	30KPA260CA	290.4	5	321.17	2	260	72.8	416
30KPA270A	30KPA270CA	301.6	5	333.52	2	270	69.5	436.2
30KPA280A	30KPA280CA	312.8	5	345.88	2	280	65.3	464
30KPA288A	30KPA288CA	321.7	5	355.76	2	288	64.5	469.9
30KPA300A	30KPA300CA	334	5	370.58	2	300	62	484
30KPA320A	30KPA320CA	356	5	395.29	2	320	57	530





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TYPICAL CHARACTERISTICS CURVES



T_J = T_J max.
8.3 ms Single Half Sine-Wave

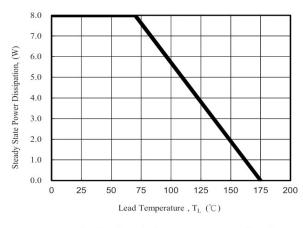
400

950
350
250
1 10 100

Number of Cycles at 60 Hz

Fig. 1 - Pulse Derating Curve

Fig. 2 - Maximum Non-Repetitive Surge Current



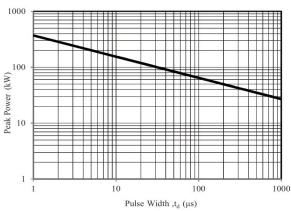
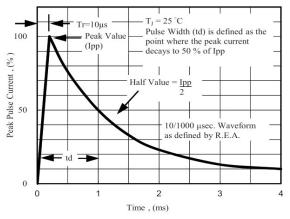


Fig. 3 - Steady State Power Derating Curve

Fig. 4 - Peak Pulse Power Rating Curve



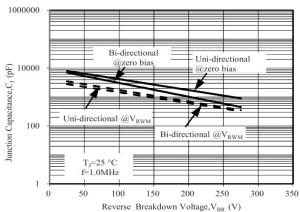
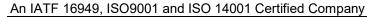


Fig. 5 - Pulse Waveform

Fig. 6 - Typical Junction Capacitance



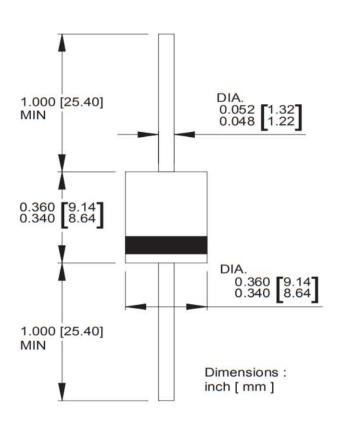






PACKGE DETAILS

R-6



Mechanical Data

- 1. Case: Molded plastic
- 2. Epoxy: UL 94V-0 rate flame retardant
- 3. Lead: Solderable per MIL-STD-202, method 208 guranteed
- 4. Polarity: Color band denotes cathode end except Bipolar
- 5. Mounting position: Any







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Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- · Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- · Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

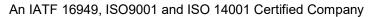
For this, the following JEDEC table may be referred:

JEDEC MSL Level						
Level	Time	Condition				
1	Unlimited	≤30 °C / 85% RH				
2	1 Year	≤30 °C / 60% RH				
2a	4 Weeks	≤30 °C / 60% RH				
3	168 Hours	≤30 °C / 60% RH				
4	72 Hours	≤30 °C / 60% RH				
5	48 Hours	≤30 °C / 60% RH				
5a	24 Hours	≤30 °C / 60% RH				
6	Time on Label(TOL)	≤30 °C / 60% RH				

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Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered trademark of

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