

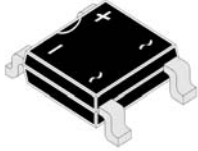


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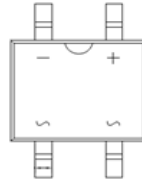
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## 1.0Amp BRIDGE RECTIFIERS Single Phase . GLASS PASSIVATED



ABS



**ABS2 - ABS10**

**ABS package  
RoHS compliant**

### FEATURES:

- 1.The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- 2.Idea for printed circuit board
- 3.Glass passivated Junction chip
- 4.Low reverse leakage
- 5.High forward surge current capability
6. High temperature soldering guaranteed 250 C/10 seconds at terminals
- 7.This product is available in AEC-Q101 Compliant also
8. NOTE: For AEC-Q101 compliant products, please use suffix -AQ in the part number while ordering



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### Maximum Ratings And Electrical Characteristics

Ratings at 25 C ambient temperature unless otherwise specified. Single phase half-wave 60Hz, resistive or inductive load for capacitive load current derate by 20%.

PARAMETERS		SYMBOL	ABS2	ABS4	ABS6	ABS8	ABS10	UNITS
Maximum repetitive peak reverse voltage		$V_{RRM}$	200	400	600	800	1000	VOLTS
Maximum RMS voltage		$V_{RMS}$	140	280	420	560	700	VOLTS
Maximum DC blocking voltage		$V_{DC}$	200	400	600	800	1000	VOLTS
Maximum average forward rectified current at $T_L=30\text{ C}$	On glass-epoxy P.C.B (Note 1)	$I_{(AV)}$	1.0				Amp	
	On aluminum substrate (Note 2)		1.2					
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)		$I_{FSM}$	35.0				Amp	
Maximum instantaneous forward voltage at 0.4A		$V_F$	0.95				VOLTS	
Maximum DC reverse current at rated DC blocking voltage	$T_A=25\text{ C}$	$I_R$	5.0				$\mu\text{A}$	
	$T_A=125\text{ C}$		500				pF	
Typical junction capacitance (Note 3)		$C_J$	18					
Typical thermal resistance		$R_{\theta JA}$	75				$^{\circ}\text{C/W}$	
Operating junction and storage temperature range		$T_J, T_{STG}$	-50 to +155				$^{\circ}\text{C}$	

#### Notes:

1. Mounted on glass epoxy PC board with 1.3\*1.3mm solder pad
2. Mounted on aluminum substrate PC board with 1.3\*1.3mm solder pad
3. Measured at 1MHz and applied reverse voltage of 4.0V D.C.



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### Typical Characteristic curves

FIG. 1- DERATING CURVE OUTPUT RECTIFIED CURRENT

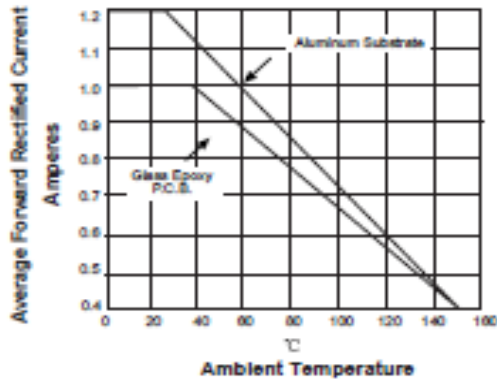


FIG. 2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT PER LEG

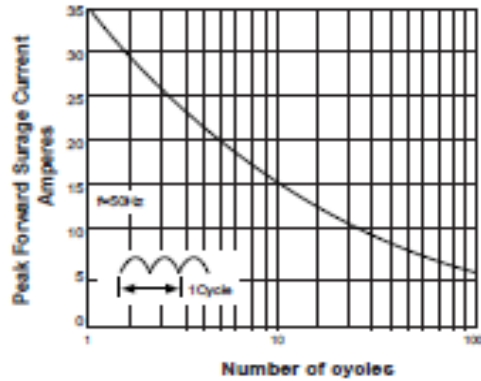


FIG. 3-TYPICAL FORWARD VOLTAGE CHARACTERISTICS

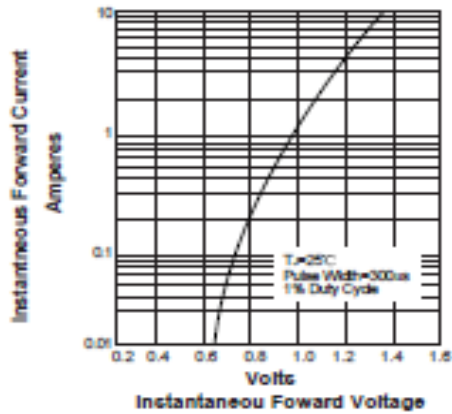


FIG. 4-TYPICAL REVERSE LEAKAGE CHARACTERISTICS

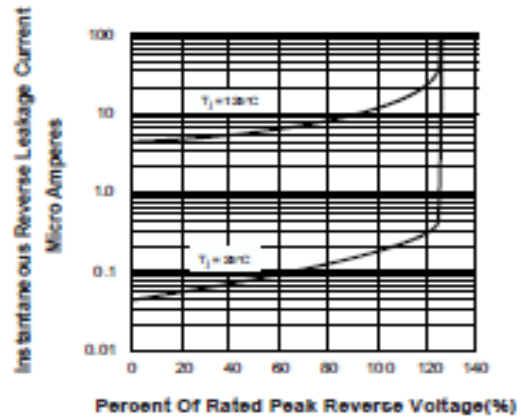
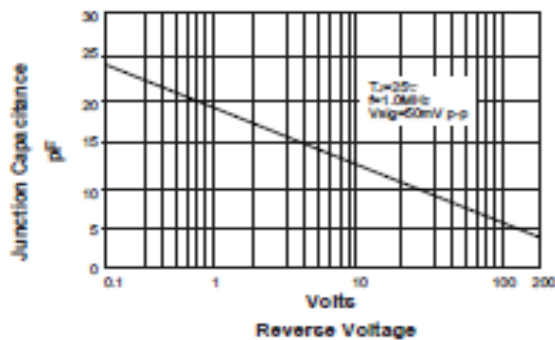


FIG. 5-TYPICAL JUNCTION CAPACITANCE





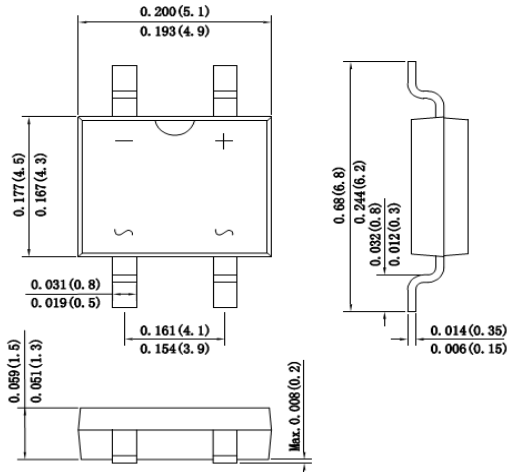
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## Package Details

### ABS



Dimensions in inches and (millimeters)

### Mechanical Data

**Case:** Molded plastic body

**Terminals:** Solder plated, solderable per MIL-STD-750, Method 2026

**Polarity:** Polarity symbol marking on body

**Mounting Position:** Any

**Weight :** 0.004 ounce, 0.12 grams



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## Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

### **Shelf Life of CDIL Products**

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

### **Floor Life of CDIL Products and MSL Level**

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤30 °C / 85%
2	1 Year	≤30 °C / 60%
2a	4 Weeks	≤30 °C / 60%
3	168 Hours	≤30 °C / 60%
4	72 Hours	≤30 °C / 60%
5	48 Hours	≤30 °C / 60%
5a	24 Hours	≤30 °C / 60%
6	Time on Label(TOL)	≤30 °C / 60%



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## Customer Notes

### Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

## Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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