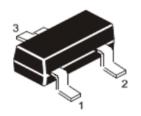
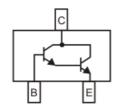






NPN SURFACE MOUNT DARLINGTON TRANSISTOR





CMBTA28 SOT-23 **Surface Mounted Plastic Package**

MARKING CODE: A28

FEATURES

- Epitaxial Planar Die Construction
- Ideal for Medium Power Amplification and Switching
- High Current Gain

MAXIMUM RATINGS @ T $_{\rm A}$ = 25°C unless otherwise specified

| PARAMETER | SYMBOL | VALUE | UNIT |
|---|------------------|------------|------|
| Collector-Base Voltage | V _{CBO} | 80 | V |
| Collector-Emitter Voltage | V _{CES} | 80 | V |
| Emitter-Base Voltage | V _{EBO} | 12 | V |
| Collector Current – Continuous | I _c | 500 | mA |
| Power Dissipation | P _d | 300 | mW |
| Thermal Resistance, Junction to Ambient | R _{θJA} | 417 | °C/W |
| Operating and Storage and Temperature Range | T_{j},T_{STG} | -55 to 150 | °C |







ELECTRICAL CHARACTERISTICS

 $T_A = 25$ °C unless otherwise specified

| PARAMETER | SYMBOL | CONDITIONS | MIN | MAX | UNIT | | | | |
|--------------------------------------|----------------------|---|-------------|-----|------|--|--|--|--|
| OFF CHARACTERISTICS (Note 1) | | | | | | | | | |
| Collector-Base Breakdown Voltage | V _{(BR)CBO} | Ι _c =100μΑ, Ι _ε =0 | 80 | | V | | | | |
| Emitter-Base Breakdown Voltage | V (BR)EBO | I _E =100μΑ, I _C =0 | 12 | | V | | | | |
| Collector-Emitter Voltage | V _{CES} | I _c =100μΑ, V _{BE} =0 | 80 | | V | | | | |
| Collector Cutoff Current | I _{CBO} | V _{CB} =60V, I _E =0 | | 100 | nA | | | | |
| Emitter Cutoff Current | I _{EBO} | V _{EB} =10V, I _C =0 | | 100 | nA | | | | |
| ON CHARACTERISTICS (Note 1) | | | | | | | | | |
| DC Current Gain | h _{FE} | I _c =10mA, V _{CE} =5.0V | 10,000 | | | | | | |
| | | I _C =100mA, V _{CE} =5.0V | 10,000 | | | | | | |
| Collector-Emitter Saturation Voltage | V _{CE(SAT)} | I _C =100mA, I _B =100μA | | 1.5 | V | | | | |
| Base- Emitter On Voltage | V _{BE(on)} | I _C =100mA, V _{CE} =5.0V | | 2.0 | V | | | | |
| SMALL SIGNAL CHARACTERISTIC | s | | | | | | | | |
| Output Capacitance | C obo | V _{CB} =10V, f=1.0MHz, I _E =0 | 8.0 Typical | | pF | | | | |
| Input Capacitance | C _{ibo} | V_{EB} =0.5V, f=1.0MHz, I_{C} =0 | 15 Typical | | pF | | | | |
| Current Gain-Bandwidth Product | f _T | V_{CE} =5.0V, I_{C} =10mA, f = 100MHz | 125 | | MHz | | | | |

Notes:

1. Pulse Width ≤ 300µs, Duty Cycle ≤ 2%





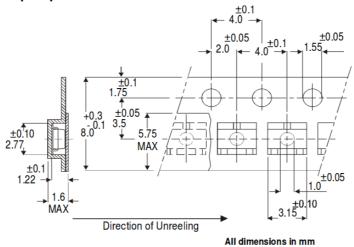


SOT-23 PACKAGE DIMENSIONS AND OUTLINE

SOT-23 Package Reel Information SOT-23 Formed SMD Package ±0.05 2.50— Reel Specifications for W Packing (13") and 7" ±0.02 0.60 7.9-10.9 0100.0±0.5/054.5 ±0.5 0329.2±0.5 /178±0.5 9.2±0.5 8 9 3 0.50 All dimensions in mm DETAIL X 330 / 180 mm - Antistatic Coated Plastic Reel 8mm Tape 8mm Tape NOTES: Size of Reel Size of Reel 330 mm (13") 180 mm (7") No. of Devices 10,000 Pcs 3,000 Pcs The bandolier of 330 mm reel contains at least 10,000 devices.

- 2. The bandolier of 180 mm reel contains at least 3,000 devices.
- No more than 0.5% missing devices / reel. 50 empty compartments for 330 mm reel.
 15 empty compartments for 180 mm reel.
- Three consecutive empty places might be found provided this gap is followed by 6 consecutive devices.
- The carrier tape (leader) starts with at least 75 empty positions (equivalent to 330 mm).
 In order to fix the carrier tape a self adhesive tape of 20 to 50 mm is applied. At the end of the bandolier at least 40 empty positions (equivalent to 160 mm) are there.

Tape Specification for SOT-23 Surface Mount Device



±0.05 2.50 ←





Customer Notes:

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

DICLAIMER

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).



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