

## SILICON EPITAXIAL PLANAR DIODES (500mW)

## CT21 ~ CT186



DO-35

**DO-35 Glass**  
**Axial Leaded Package**  
**RoHS compliant**

**Polarity :** Cathode is marked by a Band

**APPLICATION:** High Voltage Switching Diodes

### ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C unless otherwise specified)

PARAMETER	SYMBOL	VALUE	UNIT
Total Power Dissipation	$P_{TOT}$	500	mW
Operating and Storage Junction Temperature Range	$T_J, T_{STG}$	-65 to +200	°C
Peak Surge Current	$I_{FSM}$	1	A
Typical Capacitance	$C_J$	2	pF

### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise specified)

DEVICE	Peak Inverse Voltage (PIV) in V	Minimum Forward Current (I <sub>F</sub> )@1V in mA	Maximum Reverse Current (I <sub>R</sub> ) @ V <sub>R</sub> in mA	Reverse Voltage (V <sub>R</sub> ) in V	Average Rectified Current (I <sub>O</sub> ) in mA	Peak Forward Current (I <sub>FM</sub> ) in mA
CT 21	20	50	1	15	100	400
CT 22	50	50	1	40	100	400
CT 23	100	50	1	90	100	400
CT 24	150	50	1	130	100	400
CT 26	20	100	1	15	200	450
CT 27	50	100	1	40	200	450
CT 28	100	100	1	90	200	450
CT 29	150	100	1	130	200	450
CT 31	20	50	0.1	15	100	400
CT 32	50	50	0.1	40	100	400
CT 33	100	50	0.1	90	100	400
CT 34	150	50	0.1	130	100	400
CT 36	20	100	0.1	15	200	450
CT 37	50	100	0.1	40	200	450
CT 38	100	100	0.1	90	200	450
CT 39	150	100	0.1	130	200	450

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Rev02\_03022021E



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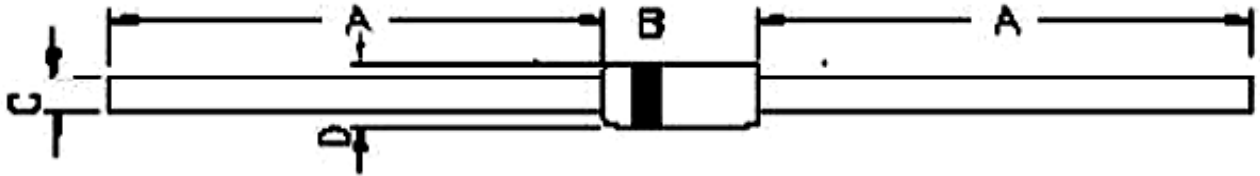
**ELECTRICAL CHARACTERISTICS** ( $T_C = 25^\circ$  unless otherwise specified)

DEVICE	Peak Inverse Voltage (PIV) in V	Peak Forward Current ( $I_F$ )		Maximum Reverse Current ( $I_R$ ) @ $V_R$ in $\mu A$	Reverse Voltage ( $V_R$ ) in V	Average Rectified Current ( $I_O$ ) in mA	Peak Forward Current ( $I_{FM}$ ) in mA
		$I_F$ @ 1V in mA	$I_F$ @ 1.1V in mA				
CT 161	30	15	--	0.25	25	60	180
CT 162	70	5	--	0.25	60	50	150
CT 163	150	1	--	0.25	125	30	125
CT 164	200	3	--	0.25	175	40	100
CT 182	40	--	100	0.25	30	100	400
CT 183	80	--	100	0.25	60	100	400
CT 184	150	--	100	0.25	125	100	400
CT 185	200	--	100	0.25	175	100	400
CT 186	250	--	100	0.25	225	100	400

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## PACKAGE DETAILS

### DO-35 Glass Axial Leaded Plastic Package



**Note:** Cathode is marked by a Band

DIM	MIN(mm)	MAX(mm)
A	27.5	--
B	--	3.9
C	--	0.5
D	--	1.9



## Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

### **Shelf Life of CDIL Products**

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

### **Floor Life of CDIL Products and MSL Level**

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤30 °C / 85% RH
2	1 Year	≤30 °C / 60% RH
2a	4 Weeks	≤30 °C / 60% RH
3	168 Hours	≤30 °C / 60% RH
4	72 Hours	≤30 °C / 60% RH
5	48 Hours	≤30 °C / 60% RH
5a	24 Hours	≤30 °C / 60% RH
6	Time on Label(TOL)	≤30 °C / 60% RH



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## Customer Notes

### Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

### Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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