

An IATF 16949, ISO9001 and ISO 14001/ISO 45001 Certified Company



1Amp GENERAL PURPOSE SILICON RECTIFIER

DSR1A~ DSR1M



SOD123 FL Surface Mount Plastic Package RoHS compliant

SOD-123 FL

FEATURES:

- 1. Glass Passivated Device
- 2. Low Reverse Leakage
- 3. Metallurgic ally Bonded Construction
- 4. High Temperature Soldering Guaranteed 250°C/10 seconds, 0.375"(9.5mm) lead length,
- 5. This product is available in AEC-Q101 Compliant and PPAP Capable also.

Note: For AEC-Q101 compliant products, please suffix - AQ in the part number while ordering

APPLICATION: General purpose switching

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	DSR 1A	DSR 1B	DSR 1D	DSR 1G	DSR 1J	DSR 1K	DSR 1M	UNIT
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V_{RSM}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum Average Rectified Forward Current at $T_A = 65^{\circ}C$	I _(AV) 1				1.0				Α
Peak Forward Surge Current (8.3ms single half sine-wave superimposed on rated load), T _L =25°C	I _{FSM}				25				Α
Maximum Instantaneous Forward Voltage at I _F = 1A	V _F				1.1				٧
Maximum DC Reverse Current at	I _R @ 25°C				10				μA
Rated DC Blocking Voltage	I _R @125°C				50				μΑ
Typical Junction Capacitance	C _J ²				4				pF
Typical Thermal Resistance	$R_{th(j-a)}^{3}$				180				K/W
Operating and Storage Junction Temperature Range	T_{j},T_{stg}				_	°C			

Note:

- 1. Average over any 20ms period.
- 2. Measured at 1MHz and applied Reverse voltage of 4.0V DC.
- 3. Thermal resistance from junction to ambient at 0.375" (9.5mm) lead length, P.C.B. Mounted.

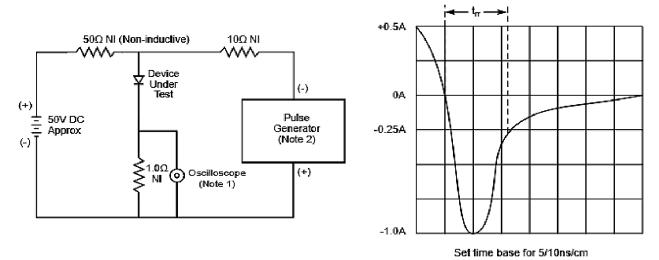






Test Circuit and Diagrams

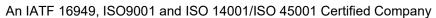
Reverse Recovery Time Characteristic and Test Circuit



Note:

- 1. RiseTime = 7.0ns max. Input Impedance = 1.0M, 22pF.
- 2. Rise Time = 10ns max. Input Impedance = 50.



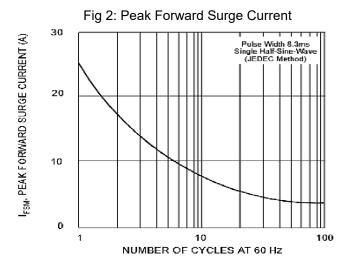


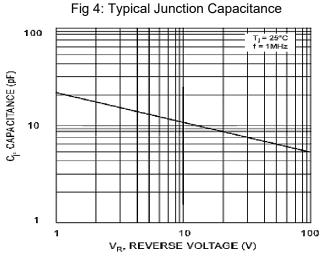




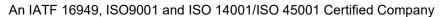
TYPICAL CHARACTERISTICS CURVES

Fig 1: Forward Derating Curve 1.0 (AN) AVERAGE FWD RECTIFIED CURRENT (A) 0.80.60.4 0.2 0 25 50 75 100 125 150 175 200 T_A, AMBIENT TEMPERATURE (°C)





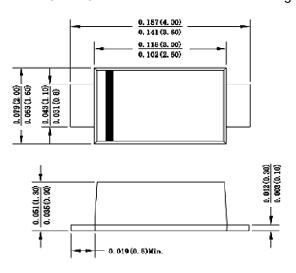






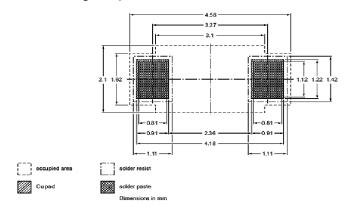
PACKAGE DETAIL

SOD-123FL Surface Mount Plastic Package

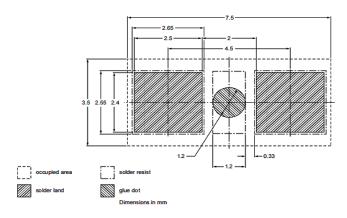


All dimensions are in inches (mm)

Reflow soldering footprint for SOD123 FL



Wave soldering footprint for SOD123 FL







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Mechanical Data

Case: Molded plastic body

Terminals: Solder plated, solderable per MIL-STD-750, Method 2026

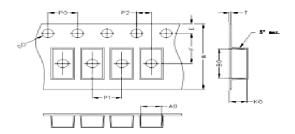
Polarity: Polarity symbol marking on body

Mounting Position: Any

Weight: 0.0007 ounce, 0.02 grams

Package information

Carrier Dimensions(mm)



A0	В0	КО	D0	E	F
2.15	3.95	1.35	1.55	1.75	3.50
P0	P1	P2	т	w	Tolerance
4.0	4.0	2.0	0.25	8	0.1

Package Specifications

Package	Reel Size	Reel DIA. (mm)	Q'TY/Reel (Kpcs)	Box Size (mm)	QTY/Box (Kpcs)	Carton Size (mm)	Q'TY/Carton (Kpcs)
SOD123FL	7'	178	3	180	15	380*200*200	150





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Recommended Reflow Solder Profiles

The recommended reflow solder profiles for Pb and Pb-free devices are shown below.

Figure 1 shows the recommended solder profile for devices that have Pb-free terminal plating, and where a Pb-free solder is used.

Figure 2 shows the recommended solder profile for devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with a leaded solder.

Figure 1

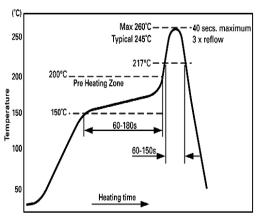
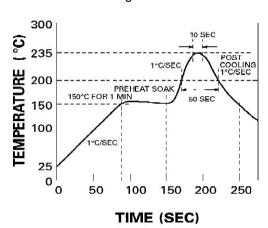


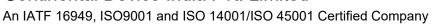
Figure 2



Reflow profiles in tabular form

Reliow profiles in tabular form				
Profile Feature	Sn-Pb System	Pb-Free System		
Average Ramp-Up Rate	~3°C/second	~3°C/second		
Preheat – Temperature Range – Time	150-170°C 60-180 seconds	150-200°C 60-180 seconds		
Time maintained above: – Temperature – Time	200°C 30-50 seconds	217°C 60-150 seconds		
Peak Temperature	235°C	260°C max.		
Time within +0 -5°C of actual Peak	10 seconds	40 seconds		
Ramp-Down Rate	3°C/second max.	6°C/second max.		

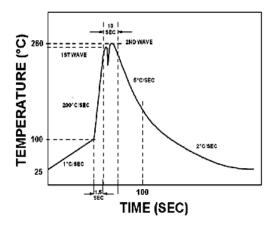




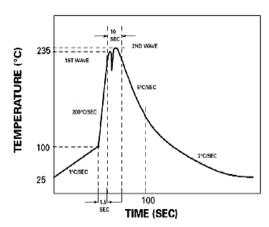


Recommended Wave Solder Profiles

The Recommended solder Profile For Devices with Pb-free terminal plating where a Pb-free solder is used



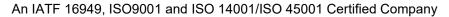
The Recommended solder Profile For Devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with leaded solder



Wave Profiles in Tabular Form

Profile Feature	Sn-Pb System	Pb-free System	
Average Ramp-Up Rate	~200°C/second	~200°C/second	
Heating rate during preheat	Typical 1-2, Max 4°C/sec	Typical 1-2, Max 4°C/Sec	
Final preheat Temperature	Within 125°C of Solder Temp	Within 125°C of Solder Temp	
Peak Temperature	235°C	260°C max.	
Time within +0 -5°C of actual Peak	10 seconds	10 seconds	
Ramp-Down Rate	5°C/second max.	5°C/second max.	







Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- · Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- · Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

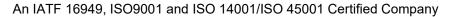
Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level				
Level	Time	Condition		
1	Unlimited	≤30 °C / 85% RH		
2	1 Year	≤30 °C / 60% RH		
2a	4 Weeks	≤30 °C / 60% RH		
3	168 Hours	≤30 °C / 60% RH		
4	72 Hours	≤30 °C / 60% RH		
5	48 Hours	≤30 °C / 60% RH		
5a	24 Hours	≤30 °C / 60% RH		
6	Time on Label(TOL)	≤30 °C / 60% RH		







Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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