

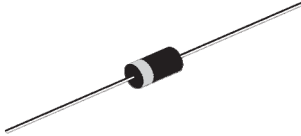
HIGH EFFICIENCY RECTIFIER

Reverse Voltage - 50 to 1000 Volts

Forward Current - 1.0 Amperes

HER101- HER108

**DO-41P Axial
Leaded Plastic Package
RoHS compliant**



DO-41P

FEATURES:

1. This product is available in AEC-Q101 Compliant and PPAP Capable also.

Note: For AEC-Q101 compliant products , please suffix - AQ in the part number while ordering

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	HER 101	HER 102	HER 103	HER 104	HER 105	HER 106	HER 107	HER 108	UNIT
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	300	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	210	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	300	400	600	800	1000	V
Maximum Average Forward Rectified Current 0.375" (9.5mm) Lead Length at Ta=55°C	$I_{(AV)}$	1.0								A
Peak Forward Surge Current, 8.3ms single half sine wave superimposed on rated load (JEDEC method)	I_{FSM}	30								A
Maximum Forward Voltage at 1.0A at 25°C	V_F	1.0		1.3		1.70				V
Maximum Reverse Current at Ta=25°C	I_R	5.0								μA
at Rated DC Blocking Voltage Ta=100°C		500								
Typical Junction Capacitance ¹	C_J	17								pF
Maximum Reverse Recovery Time ²	T_{RR}	50				75				ns
Typical Thermal Resistance ³	$R_{\theta JA}$	60								°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 TO +150								°C

Notes:

1. Measured at 1MHz and applied reverse voltage of 4.0V

2. Reverse recovery test conditions $I_F=0.5A$, $I_R=1A$, $I_{RR}=0.25A$

3. Thermal resistance junction to Ambient and from junction to lead length 0.375" (9.5mm) PCB mounted

Recommended Reflow Solder Profiles

The recommended reflow solder profiles for Pb and Pb-free devices are shown below.

Figure 1 shows the recommended solder profile for devices that have Pb-free terminal plating, and where a Pb-free solder is used.

Figure 2 shows the recommended solder profile for devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with a leaded solder.

Figure 1

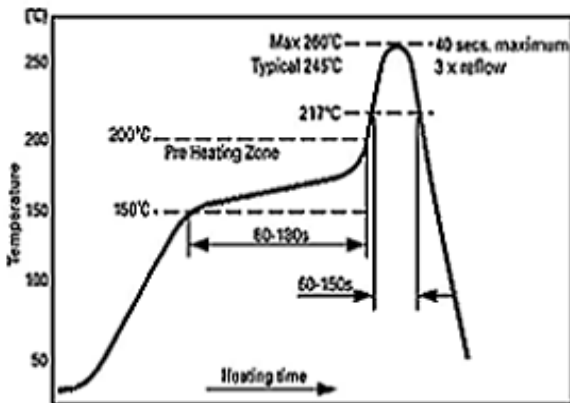
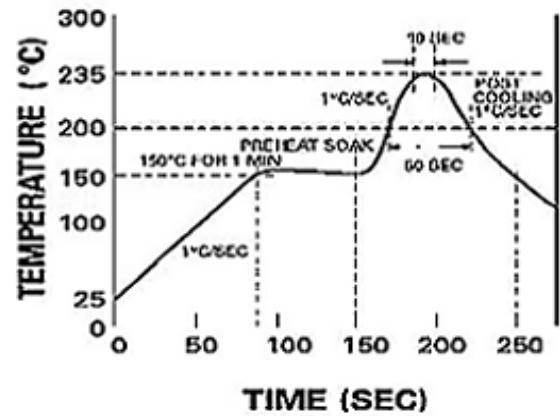


Figure 2

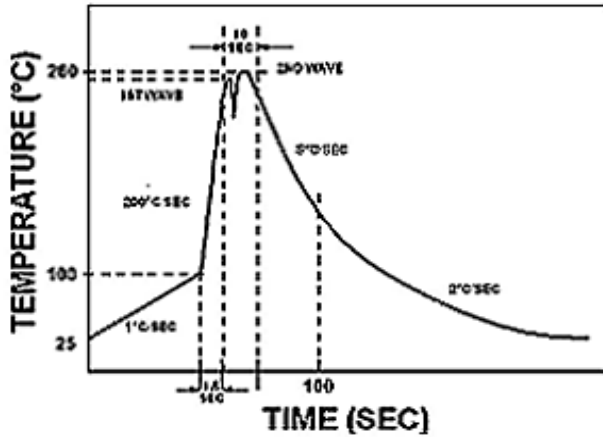


Reflow profiles in tabular form

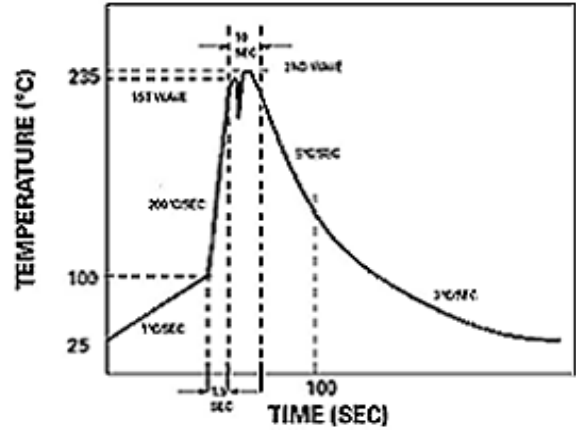
Profile Feature	Sn-Pb System	Pb-Free System
Average Ramp-Up Rate	~3°C/second	~3°C/second
Preheat		
– Temperature Range	150-170°C	150-200°C
– Time	60-180 seconds	60-180 seconds
Time maintained above:		
– Temperature	200°C	217°C
– Time	30-50 seconds	60-150 seconds
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	40 seconds
Ramp-Down Rate	3°C/second max.	6°C/second max.

Recommended Wave Solder Profiles

The Recommended solder Profile For Devices with Pb-free terminal plating where a Pb-free solder is used



The Recommended solder Profile For Devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with leaded solder



Wave Profiles in Tabular Form

Profile Feature	Sn-Pb System	Pb-Free System
Average Ramp-Up Rate	~200°C/second	~200°C/second
Heating rate during preheat	Typical 1-2, Max 4°C/sec	Typical 1-2, Max 4°C/Sec
Final preheat Temperature	Within 125°C of Solder Temp	Within 125°C of Solder Temp
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	10 seconds
Ramp-Down Rate	5°C/second max.	5°C/second max



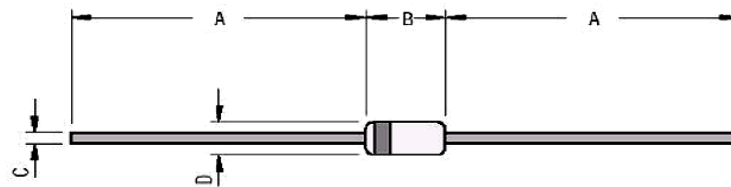
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PACKAGE DETAILS

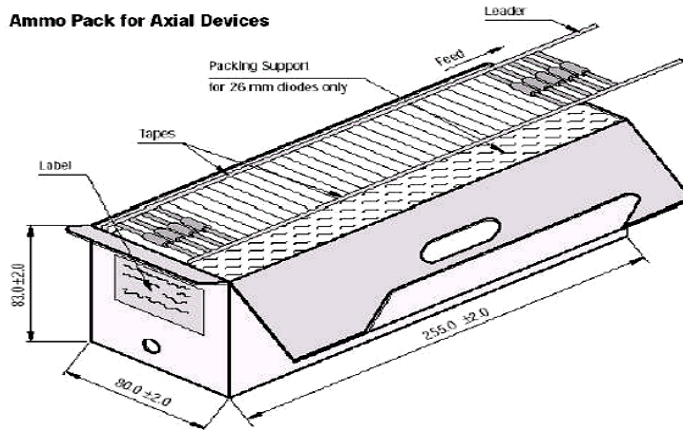
DO - 41P Axial Leaded Plastic Package



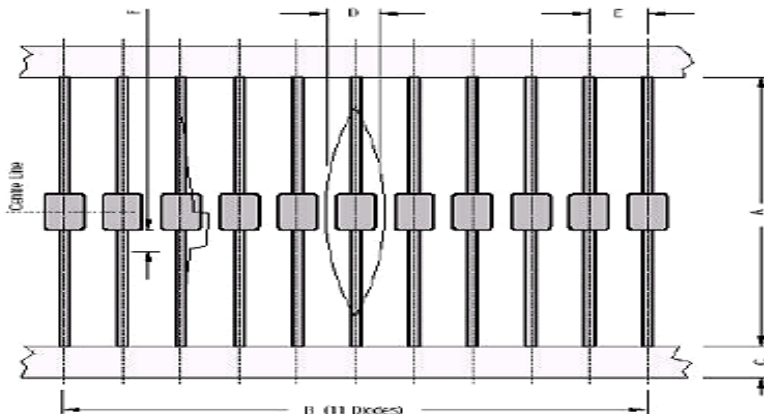
DIM	MIN	MAX
A	25.40	
B	4.20	5.20
C	0.70	0.90
D	2.00	2.70

All Dimensions are in mm

Ammo Packing for DO-41P



DO-41P Package & Packaging



DO-41P 52mm Tape

DIM	MIN	MAX
A	50.0	54.0
B	95.0	105.0
C	5.60	6.50
D		1.5R
E	9.50	10.50
F		1.25

All Dimensions are in mm

TAPE SPECIFICATIONS

- 300mm (Min) leader tape on every roll.
- No. of empty places allowed 0.25% without consecutive empty places
- Ends of leads shall normally not protrude beyond the tape
- components shall be held sufficiently in the tape or tapes so that they can not come free in normal handling.

Packing Information

Package/ Case Type	Packaging Type	Std. Packing Qty	Inner Carton		Outer Carton			
			Qty	Size L x W x H (cm)	Gross Weight (Kg)	Qty	Size L x W x H (cm)	Gross Weight (Kg)
DO-41P	T&A	5,000	5K	27 x 8 x 14	1.96	45K	46 x 35 x 25	17.5



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Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤30 °C / 85% RH
2	1 Year	≤30 °C / 60% RH
2a	4 Weeks	≤30 °C / 60% RH
3	168 Hours	≤30 °C / 60% RH
4	72 Hours	≤30 °C / 60% RH
5	48 Hours	≤30 °C / 60% RH
5a	24 Hours	≤30 °C / 60% RH
6	Time on Label(TOL)	≤30 °C / 60% RH

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Customer Notes

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered trademark of

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