





## **GLASS PASSIVATED STANDARD RECOVERY RECTIFIERS**



HR1N4001 ~ HR1N4007

DO-41P Axial Lead Plastic Package RoHS compliant

## **FEATURE:**

1. This product is available in HiREL/AEC-Q101 Compliant and PPAP Capable also.

Note: For AEC-Q101 compliant products, please use suffix -AQ in the part number while ordering.

## **APPLICATION:**

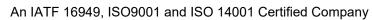
These Axial Lead Mounted Rectifiers are used for General-Purpose Low-Power Applications

## **ABSOLUTE MAXIMUM RATINGS** (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	HR1N 4001	HR1N 4002	HR1N 4003	HR1N 4004	HR1N 4005	HR1N 4006	HR1N 4007	UNIT
Peak Repetitive Reverse Voltage	$V_{RRM}$								
Working Peak Reverse Voltage	$V_{RWM}$	50	100	200	400	600	800	1000	V
DC blocking Voltage	$V_R$								1
Non-Repetitive Peak Reverse Voltage (half wave, single phase, 60Hz)	$V_{RSM}$	60	120	240	480	720	1000	1200	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	35	70	140	280	420	560	700	V
Average Rectified Current 0.375" Lead Length @ $T_a = 75$ °C				Α					
Non-Repetitive Peak Surge Current 8.3mssingle half sine-wave superimposed on rated load	I <sub>FSM</sub> 30			А					
Thermal Resistance from Junction to Ambient in free air	ion to R <sub>th(j-a)</sub> 50		°C/W						
Storage Temperature Range T <sub>stg</sub> -55 to +150			°C						
Operating Junction Temperature	$T_j$	T <sub>j</sub> -55 to +125			°C				



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## **ELECTRICAL CHARACTERISTICS** at (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Maximum Instantaneous Forward Voltage Drop	V <sub>F</sub>	I <sub>F</sub> = 1.0A	I		1.1	>
Maximum Full-Cycle Average Forward Voltage Drop	$V_{F(AV)}$	I <sub>F</sub> = 1.0A, T <sub>a</sub> = 75°C			0.8	<b>V</b>
Maximum Reverse Current	I <sub>R</sub>	At rated $V_R$ $T_A = 25^{\circ}C$ $T_A = 100^{\circ}C$	I		5 50	μΑ
Maximum Full-Cycle Average Reverse Current	I <sub>R(AV)</sub>	I <sub>O</sub> =1.0A, T <sub>a</sub> =75°C			30	μA
Junction Capacitance	C <sub>i</sub>	$V_R = 4V, f = 1MHz$		15		pF





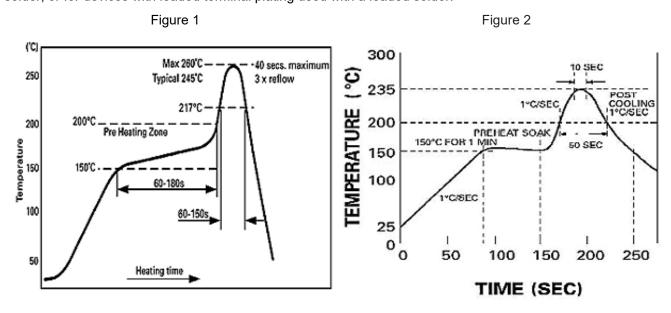


### **Recommended Reflow Solder Profiles**

The recommended reflow solder profiles for Pb and Pb-free devices are shown below.

Figure 1 shows the recommended solder profile for devices that have Pb-free terminal plating, and where a Pb-free solder is used.

Figure 2 shows the recommended solder profile for devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with a leaded solder.



## Reflow profiles in tabular form

Profile Feature	Sn-Pb System	Sn-Pb System
Average Ramp-Up Rate	~3°C/second	~3°C/second
Preheat  – Temperature Range  – Time	150-170°C 60-180 seconds	150-200°C 60-180 seconds
Time maintained above:  – Temperature  – Tim	200°C 30-50 seconds	217°C 60-150 seconds
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	40 seconds
Ramp-Down Rate	3°C/second max.	6°C/second max

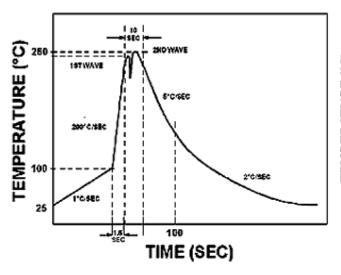


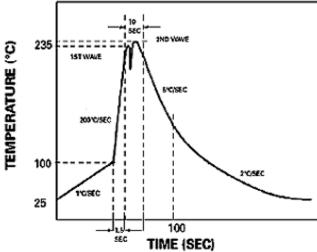




## **Recommended Wave Solder Profiles**

The Recommended solder Profile For Devices with Pbfree terminal plating where a Pb-free solder is used he Recommended solder Profile For Devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with leaded solder





## **Wave Profiles in Tabular Form**

Profile Feature	Sn-Pb System	Pb-free System		
Average Ramp-Up Rate	~200°C/second	~200°C/second		
Heating rate during preheat	Typical 1-2, Max 4°C/sec	Typical 1-2, Max 4°C/Sec		
Final preheat Temperature	Within 125°C of Solder Temp	Within 125°C of Solder Temp		
Peak Temperature	235°C	260°C max.		
Time within +0 -5°C of actual Peak	10 seconds	10 seconds		
Ramp-Down Rate	5°C/second max.	5°C/second max.		

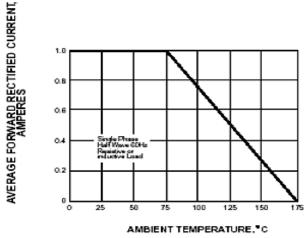




### TYPICAL CHARACTERISTICS CURVES

Fig 1: Forward Current Derating Curve

Fig 4: Maximum Non-Repetitive peak forward Surge Current



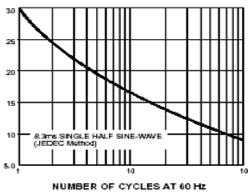
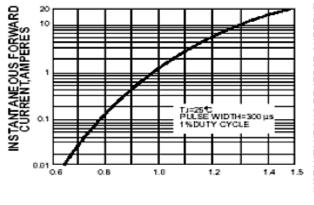
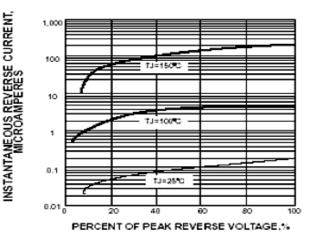


Fig 2: Typical instantaneous Forward Characteristics

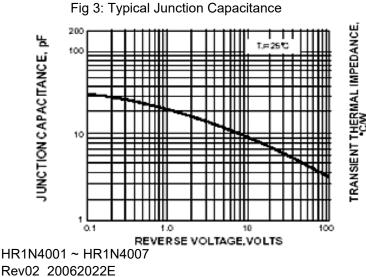
Fig 5: Typical Reverse Characteristics

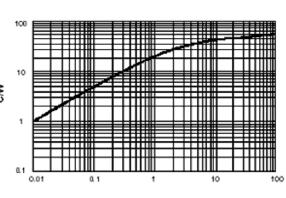




INSTANTANEOUS FORWARD VOLTAGE, VOLTS

Fig 6: Typical Transient Thermal Impedance





t,PULSE DURATION,sec.

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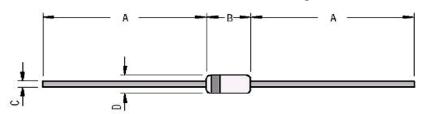






## **PACKAGE DETAILS**

## **DO-41P Axial Lead Plastic Package**



52mm - DO-41P Package

DIM	MIN	MAX
А	25.40	
В	4.20	5.20
С	0.70	0.90
D	2.00	2.70

All Dimensions are in mm



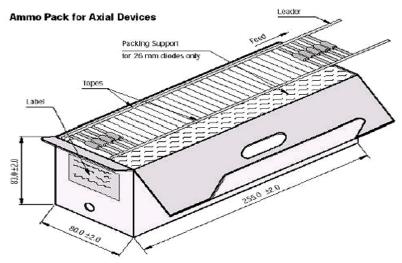
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## **Ammo Package DO-41P**



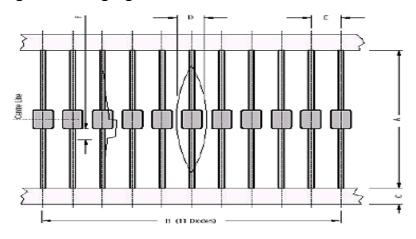
All Dimensions are in mm

## **Package Information**

Package/	Packaging Type	Std. Packing		Inner Carto	n		Outer Car	ton
Case Type		Qty	Qty	Size L x W x H	<b>Gross Weight</b>	Qty	Size L x W x H	<b>Gross Weight</b>
				(cm)	(Kg)		(cm)	(Kg)
DO-41P	T&A	5,000	5K	27 x 8 x 14	1.96	45K	46 x 35 x 25	17.5

T & A: Tape and Ammo Pack

## **DO-41P Package & Packaging**



D	DO-41P 52 mm Tape				
DIM	Min	Max			
A	50.0	54.0			
В	95.0	105.0			
С	5.60	6.50			
D		1.5R			
E	9.50	10.50			
F		1.25			

All Dimensions are in mm

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### TAPE SPECIFICATIONS

- 1. 300 mm (Min) leader tape on every roll.
- 2. No. of empty places allowed 0.25% without consecutive empty places.
- 3. Ends of leads shall normally not protrude beyond the tapes.
- Components shall be held sufficiently in the tape or tapes so that they can not come free in normal handling.







# Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- · Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- · Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- $\cdot\,$  The product shall be stored on a plane area. They should not be turned upside down.

They should not be placed against the wall.

## **Shelf Life of CDIL Products**

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

#### Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level						
Level	Time	Condition				
1	Unlimited	≤30 °C / 85% RH				
2	1 Year	≤30 °C / 60% RH				
2a	4 Weeks	≤30 °C / 60% RH				
3	168 Hours	≤30 °C / 60% RH				
4	72 Hours	≤30 °C / 60% RH				
5	48 Hours	≤30 °C / 60% RH				
5a	24 Hours	≤30 °C / 60% RH				
6	Time on Label(TOL)	≤30 °C / 60% RH				

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### **Customer Notes**

### **Component Disposal Instructions**

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

## **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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