

SILICON BRIDGE RECTIFIERS

Reverse Voltage - 50 to 1000 Volts, Forward Current - 35.0 Amperes





KBPC

ISO 14001 - ISO 45001

KBPC35005 ~ KBPC3510

KBPC Leaded Metal Case Package RoHS compliant

FEATURES:

- 1. The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- 2. Ideal for printed circuit boards
- 3. Low reverse leakage
- 4. High forward surge current capability
- 5. High temperature soldering guaranteed: 260 °C/10 seconds, at 5 lbs. (2.3kg) tension
- 6. Electrically isolated metal case for maximum heat dissipation

APPLICATIONS: Single phase input rectification.





MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(Ratings at T_a = 25°C unless specified otherwise, single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.)

| DESCRIPTION | | SYMBOL | KBPC 35005 | KBPC 3501 | KBPC 3502 | KBPC 3504 | KBPC 3506 | KBPC 3508 | KBPC 3510 | UNIT |
|--|-----------------------|------------------------|---------------|--------------|--------------|-----------------|------------------|--------------|--------------|------|
| Maximum repetitive peak reverse volt. | | V_{RRM} | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | V |
| Maximum RMS voltage | | V_{RMS} | 35 | 70 | 140 | 280 | 420 | 560 | 700 | V |
| Maximum DC blocking voltage | | V _{DC} | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | V |
| Maximum average forward output rectified current at T _c =50 °C ^{1,2} | | $I_{(AV)}$ | 35 | | | | А | | | |
| Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method) | | I _{FSM} | 400 | | | | A | | | |
| Rating for Fusing(t<8.3ms) | | l ² t | 664 | | | | A ² s | | | |
| Maximum instantaneous forward voltage drop per bridge element at 17.5A | | V_{F} | 1.1 | | | | V | | | |
| Maximum DC reverse current | T _A =25°C | - I _R - | 10 | | | | μA | | | |
| at rated DC blocking voltage | T _A =100°C | | 1 | | | | mA | | | |
| Isolation voltage from case to leads | | V _{ISO} | 2500 | | | V _{AC} | | | | |
| Typical Thermal Resistance 2 | | $R_{_{	ext{	heta}JA}}$ | 2 | | | °C/W | | | | |
| Operating junction temperature range | | TJ | -65 to +150 | | | °C | | | | |
| Storage temperature range | | T _{STG} | -65 to +150 | | | | °C | | | |

NOTES:

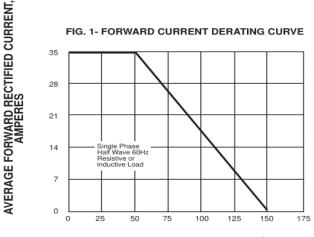
1.Unit mounted on 9" x 3.5" x4.6" thick(23cmx9cmx11.8cmcm)Al.plate.

2.Bolt down on heat-sink with silicone thermal compound between bridge and mounting surface for maximum heat transfer efficiency with #8 screw.



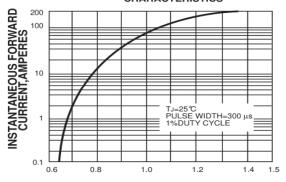


TYPICAL CHARACTERISTIC CURVES



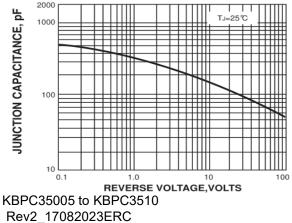
AMBIENT TEMPERATURE, °C

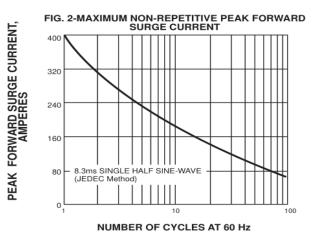
FIG. 3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

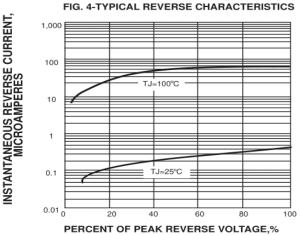


INSTANTANEOUS FORWARD VOLTAGE, VOLTS

FIG. 5-TYPICAL JUNCTION CAPACITANCE





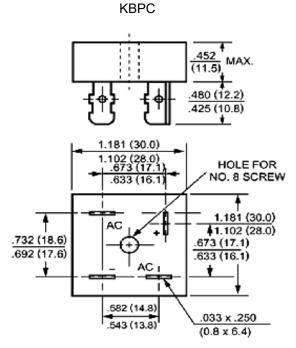


t,PULSE DURATION,sec.





PACKAGE DETAILS



Dimensions in inches and (millimeters)

MECHANICAL DATA

Case: Metal or molded plastic with heatsink integrally mounted in the bridge encapsulation Terminals: Either plated 0.25" (6.35mm) Fasten lugs or plated copper leads 0.040" (1.02mm) diameter. Mounting position: Thru hole for #8 screw mounting

Weight: 30 gram approx.



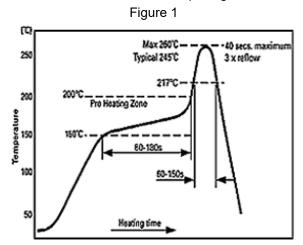


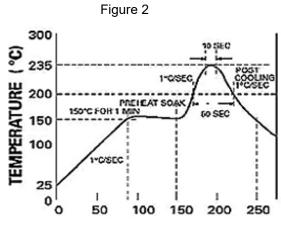
Recommended Reflow Solder Profiles

The recommended reflow solder profiles for Pb and Pb-free devices are shown below.

Figure 1 shows the recommended solder profile for devices that have Pb-free terminal plating, and where a Pb-free solder is used.

Figure 2 shows the recommended solder profile for devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with a leaded solder.





TIME (SEC)

Reflow profiles in tabular form

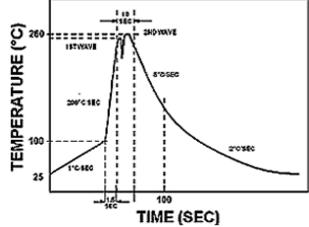
| Profile Feature | Sn-Pb System | Pb-Free System |
|--|-----------------------------|-----------------------------|
| Average Ramp-Up Rate | ~3°C/second | ~3°C/second |
| Preheat – Temperature Range – Time | 150-170°C 60-180 seconds | 150-200°C 60-180 seconds |
| Time maintained above: – Temperature – Tim | 200°C 30-50 seconds | 217°C 60-150 seconds |
| Peak Temperature | 235°C | 260°C max. |
| Time within +0 -5°C of actual | 10 seconds | 40 seconds |
| Ramp-Down Rate | 3°C/second max. | 6°C/second max |

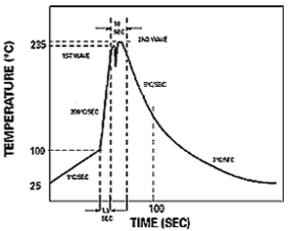




Recommended Wave Solder Profiles

The Recommended solder Profile For Devices with Pb-free terminal plating where a Pb-free solder is used The Recommended solder Profile For Devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with leaded solder





Wave Profiles in Tabular Form

| Profile Feature | Sn-Pb System | Pb-free System | | |
|-------------------------------|-----------------------------|-----------------------------|--|--|
| Average Ramp-Up Rate | ~200°C/second | ~200°C/second | | |
| Heating rate during preheat | Typical 1-2, Max 4°C/sec | Typical 1-2, Max 4°C/Sec | | |
| Final preheat Temperature | Within 125°C of Solder Temp | Within 125°C of Solder Temp | | |
| Peak Temperature | 235°C | 260°C max. | | |
| Time within +0 -5°C of actual | 10 seconds | 10 seconds | | |
| Ramp-Down Rate | 5°C/second max. | 5°C/second max. | | |





Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- \cdot Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- $\cdot\,$ Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- $\cdot\,$ The product shall be stored on a plane area. They should not be turned upside down.
- They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start. For this, the following JEDEC table may be referred:

| JEDEC MSL Level | | | | |
|-----------------|--------------------|-----------------|--|--|
| Level | Time | Condition | | |
| 1 | Unlimited | ≤30 °C / 85% RH | | |
| 2 | 1 Year | ≤30 °C / 60% RH | | |
| 2a | 4 Weeks | ≤30 °C / 60% RH | | |
| 3 | 168 Hours | ≤30 °C / 60% RH | | |
| 4 | 72 Hours | ≤30 °C / 60% RH | | |
| 5 | 48 Hours | ≤30 °C / 60% RH | | |
| 5a | 24 Hours | ≤30 °C / 60% RH | | |
| 6 | Time on Label(TOL) | ≤30 °C / 60% RH | | |





Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL wil not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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