



Single Phase Glass Passivated Bridge Rectifiers

200V-1000V, 0.8A

MB2S-MB10S

MBS SMD Package RoHS compliant



FEATURES:

- 1. The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- 2. Idea for printed circuit board
- 3. Glass passivated Junction chip
- 4. Low reverse leakage
- 5. High forward surge current capability
- 6. High temperature soldering guaranteed 250°C/10 seconds at terminals
- 7. This product is available in AECQ-101 compliant also.
- 8. Note: For AECQ-101 compliant product, please suffix-AQ in the part number while ordering

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

| PARAMETERS | | SYMBOL | MB2S | MB4S | MB6S | MB8S | MB10S | UNITS |
|---|-------------------|------------------|-------------|------|------|------|-------|-------|
| Maximum repetitive peak re | V_{RRM} | 200 | 400 | 600 | 800 | 1000 | V | |
| Maximum RMS voltage | | V_{RMS} | 140 | 280 | 420 | 560 | 700 | V |
| Maximum DC blocking volta | ige | V_{DC} | 200 | 400 | 600 | 800 | 1000 | V |
| Maximum average forward TL=100° C On glass-epoxy | I _(AV) | 0.8 | | | Α | | | |
| Peak forward surge current 8.3ms single half sine-wave superimposed on rated load | | I _{FSM} | 30.0 | | | Α | | |
| Rating for fusing (t=8.3ms, Ta=25° C) | | l ² t | 3.74 | | | Α | | |
| Maximum instantaneous forward voltage at 0.4A | | V _F | 1.10 | | | ٧ | | |
| Maximum DC reverse T _A =25°C | | | | | 5.0 | | | |
| current at rated DC blocking voltage T _A =125°C | | I _R | 500 | | | | μΑ | |
| Typical junction capacitance | CJ | 15.0 | | | рF | | | |
| Typical thermal resistance | | $R_{\theta JA}$ | 85.0 | | | °C/W | | |
| Operating junction and storage temperature range | | T_J, T_{STG} | -55 to +155 | | | °C | | |

Notes:

- 1. Mounted on glass epoxy PC board with 1.3*1.3mm solder pad
- 2.Measured at 1MHz and applied reverse voltage of 4.0V D.C.



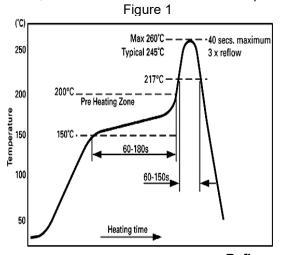


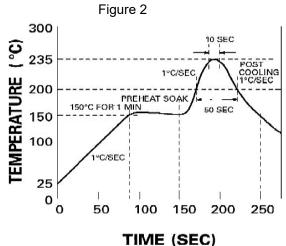
Recommended Reflow Solder Profiles

The recommended reflow solder profiles for Pb and Pb-free devices are shown below.

Figure 1 shows the recommended solder profile for devices that have Pb-free terminal plating, and where a Pb-free solder is used.

Figure 2 shows the recommended solder profile for devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with a leaded solder.





Reflow profiles in tabular form

| Kenow promes in tabular form | | | | | | |
|---|------------------------|-------------------------|--|--|--|--|
| Profile Feature | Sn-Pb System | Pb-Free System | | | | |
| Average Ramp-Up Rate | ~3°C/second | ~3°C/second | | | | |
| Preheat | | | | | | |
| – Temperature Range | 150-170°C | 150-200°C | | | | |
| – Time | 60-180 seconds | 60-180 seconds | | | | |
| Time maintained above: – Temperature – Time | 200°C 30-50 seconds | 217°C 60-150 seconds | | | | |
| Peak Temperature | 235°C | 260°C max. | | | | |
| Time within +0 -5°C of actual Peak | 10 seconds | 40 seconds | | | | |
| Ramp-Down Rate | 3°C/second max. | 6°C/second max | | | | |



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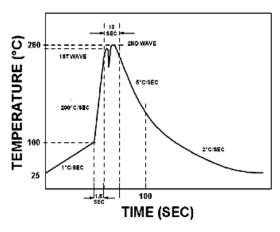


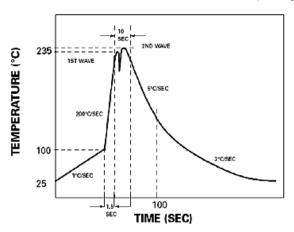


An IATF 16949, ISO9001 and ISO 14001 Certified Company

Recommended Wave Solder Profiles

The Recommended solder Profile For Devices with Pb-free terminal plating





| Wave Profiles in Tabular Form | | | | | | |
|------------------------------------|-----------------------------|-----------------------------|--|--|--|--|
| Profile Feature | Sn-Pb System | Pb-free System | | | | |
| Average Ramp-Up Rate | ~200°C/second | ~200°C/second | | | | |
| Heating rate during preheat | Typical 1-2, Max 4°C/sec | Typical 1-2, Max 4°C/Sec | | | | |
| Final preheat Temperature | Within 125°C of Solder Temp | Within 125°C of Solder Temp | | | | |
| Peak Temperature | 235°C | 260°C max. | | | | |
| Time within +0 -5°C of actual Peak | 10 seconds | 10 seconds | | | | |
| Ramp-Down Rate | 5°C/second max. | 5°C/second max. | | | | |





TYPICAL CHARACTERISTIC CURVES

Fig 1: Derating Curve Output Rectified Current

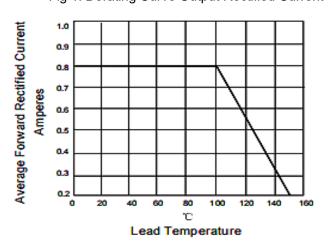


Fig 3: Maximum Non-Repetitive Peak Forward Surge Current Per leg

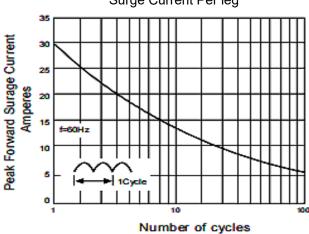


Fig 2: Typical Forward Voltage Characteristics

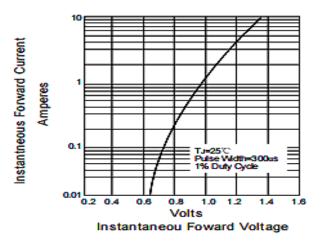
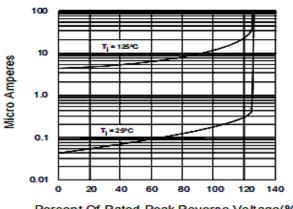


Fig 4: Typical Reverse Leakage Characteristics



Percent Of Rated Peak Reverse Voltage(%)

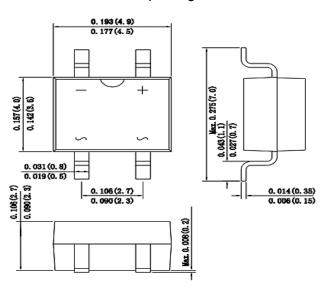
MB2S-MB10S Rev01 16072022 ESW nstantaneous Reverse Leakage Current





Package Details

MBS package



Dimensions in inches and (millimeters)

Mechanical Data

Case: Molded plastic body

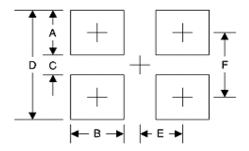
Terminals: Solder plated, solderable per MIL-STD-750, Method 2026

Polarity: Polarity symbol marking on body

Mounting Position: Any

Weight: 0.004 ounce, 0.118 grams

Suggested Pad layout



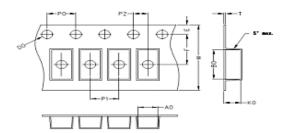
| Symbol | Unit (mm) | Unit (inch) |
|--------|-----------|-------------|
| Α | 1.7 | 0.067 |
| В | 1.0 | 0.039 |
| С | 4.40 | 0.173 |
| D | 8.10 | 0.319 |
| E | 1.25 | 0.049 |
| F | 6.30 | 0.248 |





Package Information

Carrier Dimension(mm)



| A0 | B0 | K0 | D0 | E | F |
|------|------|------|------|------|-----------|
| 5.10 | 7.20 | 2.88 | 1.55 | 1.75 | 5.50 |
| P0 | P1 | P2 | Т | w | Tolerance |
| 4.0 | 8.0 | 2.0 | 0.25 | 12 | 0.1 |

Package Specifications

| Package | Reel Size | Reel DIA. (mm) | Q'TY/Reel (Kpcs) | Box Size (mm) | QTY/Box (Kpcs) | Carton Size (mm) | Q'TY/Carton (Kpcs) |
|---------|-----------|-------------------|---------------------|------------------|-------------------|---------------------|-----------------------|
| MBS | 13' | 330 | 3 | 338 | 6 | 365*365*360 | 48 |





Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- · Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- · Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

| JEDEC MSL Level | | | | | |
|-----------------|--------------------|-----------------|--|--|--|
| Level | Time | Condition | | | |
| 1 | Unlimited | ≤30 °C / 85% RH | | | |
| 2 | 1 Year | ≤30 °C / 60% RH | | | |
| 2a | 4 Weeks | ≤30 °C / 60% RH | | | |
| 3 | 168 Hours | ≤30 °C / 60% RH | | | |
| 4 | 72 Hours | ≤30 °C / 60% RH | | | |
| 5 | 48 Hours | ≤30 °C / 60% RH | | | |
| 5a | 24 Hours | ≤30 °C / 60% RH | | | |
| 6 | Time on Label(TOL) | ≤30 °C / 60% RH | | | |





Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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