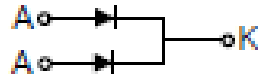




Schottky Barrier Rectifiers

MBR20100CT

TO-220AB



TO-220AB
Plastic Package
RoHS compliant

FEATURES:

MBR20100CT Device optimized for ultra-low forward voltage drop to maximize efficiency in Power Supply applications.

1. Multilayer Metal -Silicon Potential Structure.
2. Low Leakage Current
3. High Current Capability, High Efficiency
4. High Junction Temperature Capability.

APPLICATIONS:

1. Low Voltage High Frequency Switching Power Supply
2. Low Voltage High Frequency Invers Circuit.
3. Low Voltage Continued Circuit and Protection Circuit.

ABSOLUTE MAXIMUM RATINGS ($T_a = 25\text{ }^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Maximal Inverted Repetitive Peak Voltage	V_{RRM}	100	V
*Average Rectified Forward Current (Rated VR-20Khz Square Wave) - 50% duty cycle	I_{FAV}	20	A
Forward Peak Surge Current(Rated Load 8.3 Half Mssine Wave-According to JEDEC Method)	I_{FSM}	260X2	A
Operating Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-40~150	$^\circ\text{C}$
Typical Thermal Resistance(per leg) Package=TO-220AB	$R_{\theta JC}$	2	$^\circ\text{C/W}$

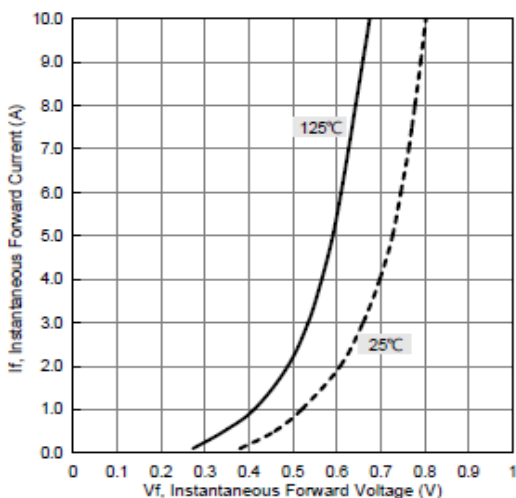


ELECTRICAL CHARACTERISTICS at $T_a = 25^\circ\text{C}$

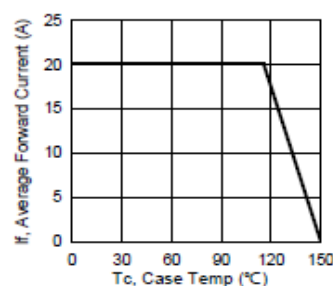
Parameter		Symbol	Test Conditions	Value			Unit
				Min.	Typ.	Max.	
Break down voltage	$T_J = 25^\circ\text{C}$	V_B	$I_R = 1\text{mA}$	100	--	--	V
Reverse Current	$T_J = 25^\circ\text{C}$	I_R	$V_R = 100\text{V}$	--	--	0.01	mA
	$T_J = 125^\circ\text{C}$			--	--	10.00	
Forward Voltage Drop	$T_J = 25^\circ\text{C}$	V_F	$I_F = 10\text{A}$ ($I_{FAV} = 10\text{A} \times 2$)	--	--	0.85	V
	$T_J = 125^\circ\text{C}$			--	--	0.75	

CHARACTERISTIC CURVES

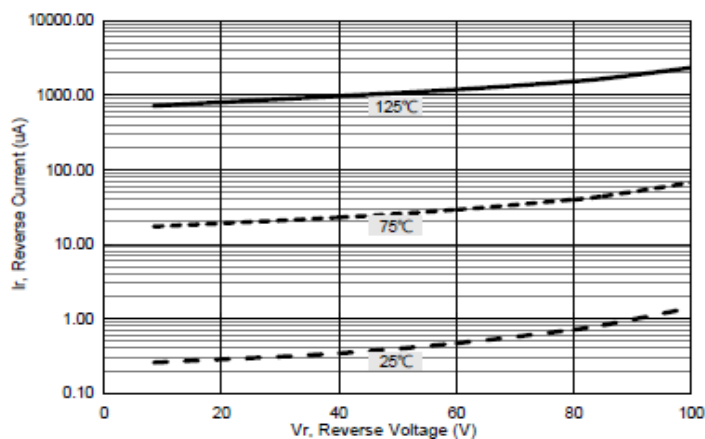
The forward voltage and forward current curve



Current derating curve, per element



The reverse leak current and the reverse voltage (single-device) curve.

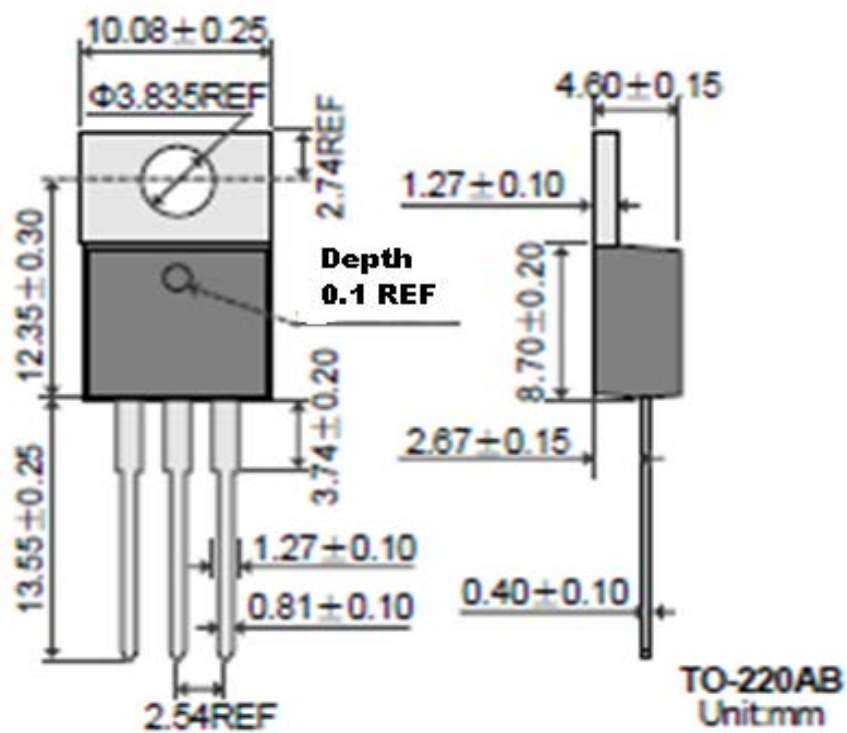




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Package Details



TO-220AB

Note: For AECQ compliant products, please suffix -AH in the part number while ordering

MBR20100CT
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Recommended Product Storage Environment for Diode and Transistors

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years, the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start. For this the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤ 30 °C / 85% RH
2	1 Year	≤ 30 °C / 60% RH
2a	4 Weeks	≤ 30 °C / 60% RH
3	168 Hours	≤ 30 °C / 60% RH
4	72 Hours	≤ 30 °C / 60% RH
5	48 Hours	≤ 30 °C / 60% RH
5a	24 Hours	≤ 30 °C / 60% RH
6	Time on Label(TOL)	≤ 30 °C / 60% RH

Figure 1 Floor Life according to JEDEC MSL Level



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Customer Notes

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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