

4A GLASS PASSIVATED HIGH EFFICIENCY RECTIFIERS



DO-201AD

MUR440
MUR460

DO-201AD
Axial Plastic Package
RoHS compliant

FEATURES:

1. Glass Passivated Junction Chip
2. Plastic Package has Underwriters Laboratories Flammability Classification 94V-0
3. Ideal Suited for use in very High Frequency Switching Power Supplies, Inverters and as Free Wheeling Diodes
4. Ultra Fast Recovery time for High Efficiency
5. Polarity : Colour band denotes Cathode end
6. Terminals : Plated Axial Leads, Solderable per MIL-STD-750, Method 2026
7. High Temperature Soldering Guaranteed : 250°C/10 Seconds, 0.375" (9.5mm) Lead Length 5 lbs. (2.3Kg) tension
8. Weight : 1.2g
5. This product is available in AEC-Q101 Compliant and PPAP Capable also.

Note: For AEC-Q101 compliant products, please use suffix -AQ in the part number while ordering.

ABSOLUTE MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(T_a = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	MUR440	MUR460	UNIT
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	400	600	V
Maximum RMS Voltage	V _{RMS}	280	420	V
Maximum DC Blocking Voltage	V _{DC}	400	600	V
Maximum Average Forward Rectified Current	I _{F(AV)}	4		A
Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	150		A
Maximum Forward Voltage at Rated Forward Current	V _F	1.35		V
Maximum Reverse Recovery Time ¹	t _{rr}	60		ns
Typical Thermal Resistance, Junction to Ambient	R _{thJA}	28		°C/W
Maximum DC Reverse Current at rated DC Blocking Voltage	T _A = 25°C	10		µA
	T _A = 125°C	300		µA
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 to +150		°C

Note:

1. Reverse Recovery Condition I_F = 0.5A, I_R = 1.0A, I_{RR} = 0.25A
2. Lead Length = 1/2" on P.C. Board with 1.5" X 1.5" Copper Surface

Recommended Reflow Solder Profiles

The recommended reflow solder profiles for Pb and Pb-free devices are shown below.

Figure 1 shows the recommended solder profile for devices that have Pb-free terminal plating, and where a Pb-free solder is used.

Figure 2 shows the recommended solder profile for devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with a leaded solder.

Figure 1

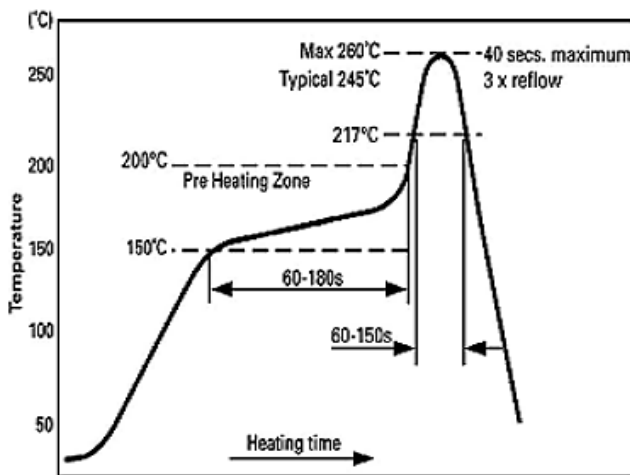
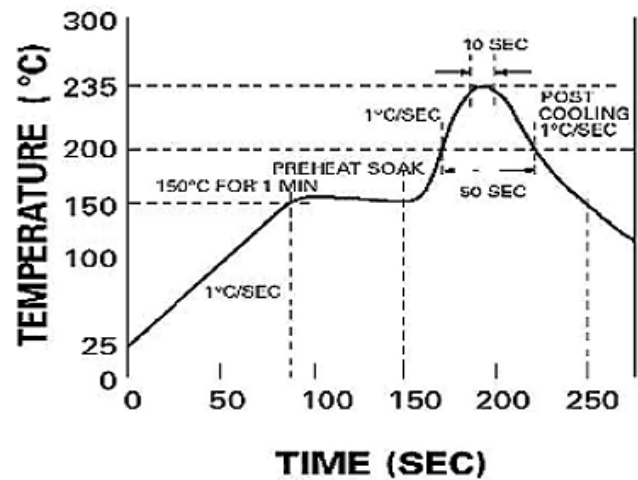


Figure 2



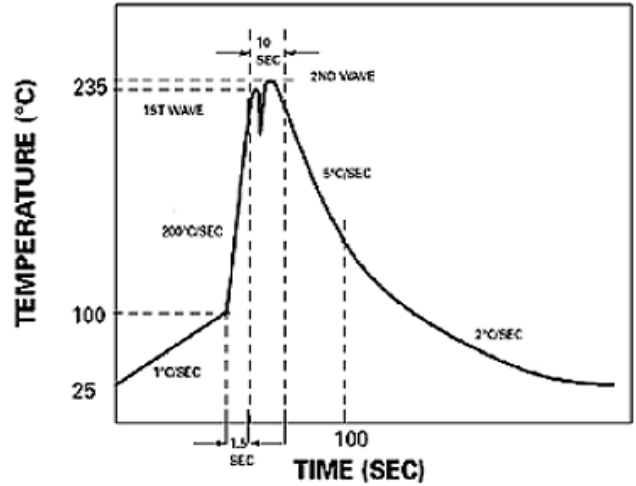
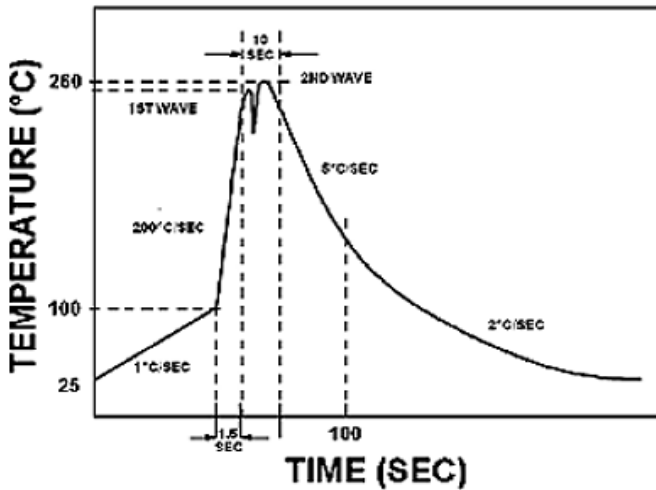
Reflow profiles in tabular form

Profile Feature	Sn-Pb System	Pb-Free System
Average Ramp-Up Rate	~3°C/second	~3°C/second
Preheat		
– Temperature Range	150-170°C	150-200°C
– Time	60-180 seconds	60-180 seconds
Time maintained above:		
– Temperature	200°C	217°C
– Time	30-50 seconds	60-150 seconds
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	40 seconds
Ramp-Down Rate	3°C/second max.	6°C/second max.

Recommended Wave Solder Profiles

The Recommended solder Profile For Devices with Pb-free terminal plating where a Pb-free solder is used

The Recommended solder Profile For Devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with leaded solder



Wave Profiles in Tabular Form

Profile Feature	Sn-Pb System	Pb-Free System
Average Ramp-Up Rate	~200°C/second	~200°C/second
Heating rate during preheat	Typical 1-2, Max 4°C/sec	Typical 1-2, Max 4°C/Sec
Final preheat Temperature	Within 125°C of Solder Temp	Within 125°C of Solder Temp
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	10 seconds
Ramp-Down Rate	5°C/second max.	5°C/second max

TYPICAL CHARACTERISTICS CURVES

Fig 1: Forward Current Derating Curve

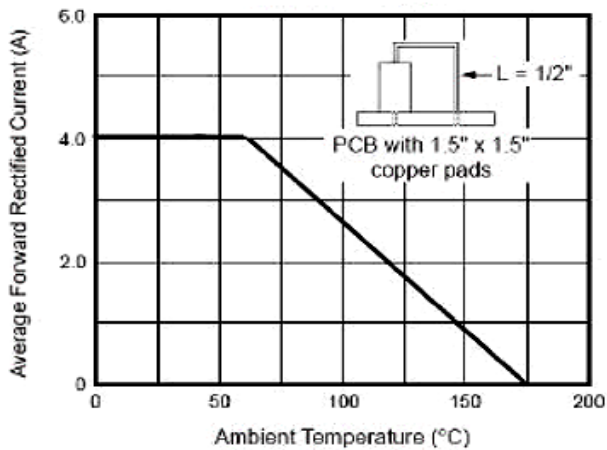


Fig 4: Maximum Non-Repetitive Peak Forward Surge Current

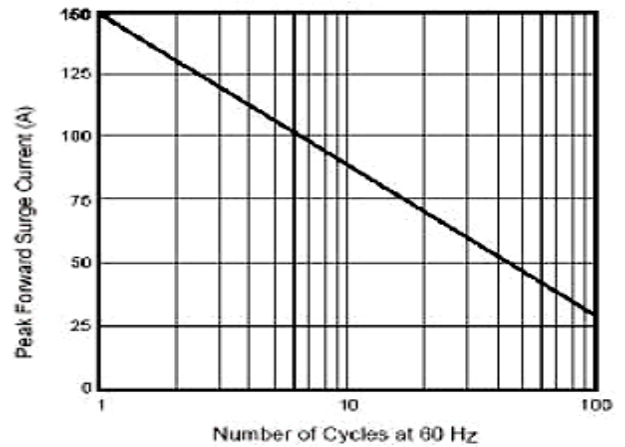


Fig 2: Typical Instantaneous Forward Characteristics

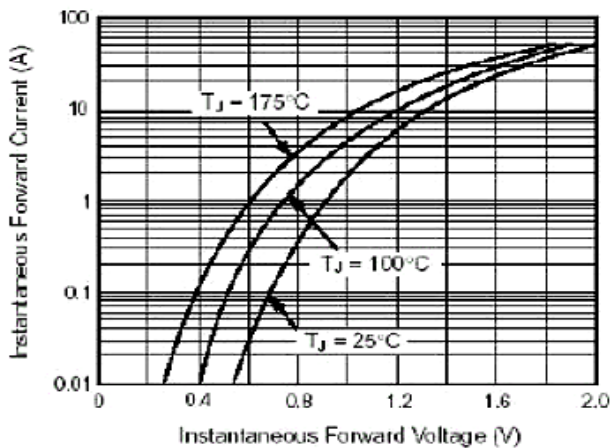


Fig 5: Typical Reverse Characteristics

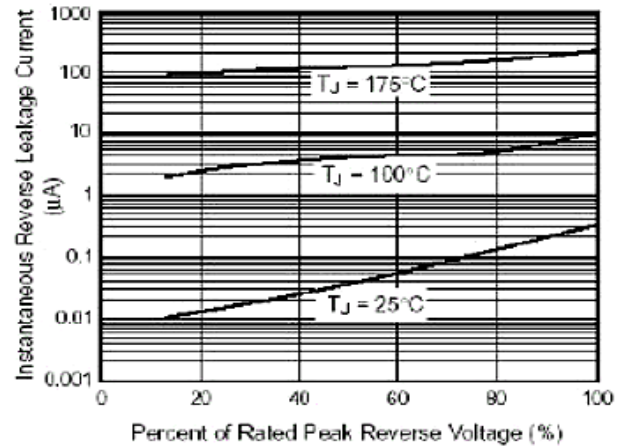
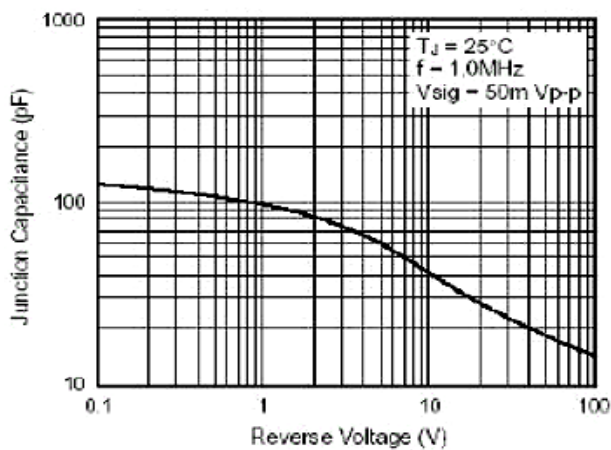
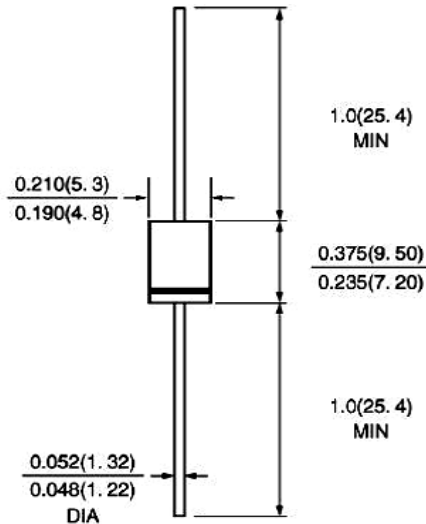


Fig 3: Typical Junction Capacitance Per Leg



PACKAGE DETAILS

DO-201AD Axial Plastic Package



Dimensions in Inches and (Millimeters)

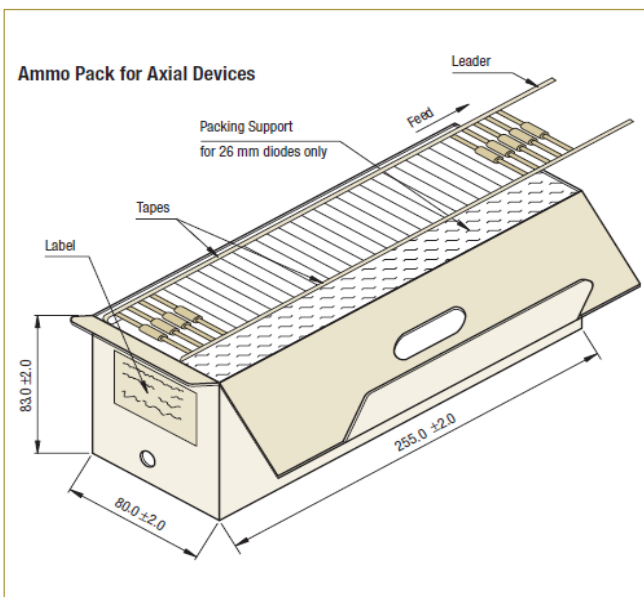
Packaging Specifications:

T & A: Tape and Ammo Pack; T & R: Tape and Reel; Bulk: Loose in Poly Bags; Tube: Tube and Carton; K: 1,000

Package / Case Type	Packaging Type	Std. Packing Qty	Inner Carton			Outer Carton		
			Qty	Size L x W x H (cm)	Gross Weight (Kg)	Qty	Size L x W x H (cm)	Gross Weight (Kg)
DO-201AD	T & A	1,250	1.25K	29 x 8 x 15	1.7	10.8K	46 x 36 x 25	15.3

Axial Tape Specifications

Device	Type	A		B		C		D		E		F	
		mm	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min
DO-201AD	52 mm	50.0	54.0	95.0	105.0	5.6	6.5	—	1.5R	9.5	10.5	—	1.3



Taping Specification

- 300 mm (Min) leader tape on every roll.
- No. of empty places allowed 0.25% without consecutive empty places.
- Ends of leads shall normally not protrude beyond the tapes.
- Components shall be held sufficiently in the tape or tapes so that they can not come free in normal handling.

MUR440_460
Rev01_06072022E



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Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤30 °C / 85% RH
2	1 Year	≤30 °C / 60% RH
2a	4 Weeks	≤30 °C / 60% RH
3	168 Hours	≤30 °C / 60% RH
4	72 Hours	≤30 °C / 60% RH
5	48 Hours	≤30 °C / 60% RH
5a	24 Hours	≤30 °C / 60% RH
6	Time on Label(TOL)	≤30 °C / 60% RH

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Customer Notes

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information.

Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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