

STANDARD CAPACITANCE TVS

An IATF 16949, ISO9001 and ISO 14001 Certified Company







SOD-323GW

SOD-323GW Plastic Package RoHS compliant

FEATURES:

- 1. Small SOD-323 Package
- 2. Bidirectional Configurations
- 3. Peak Power Dissipation 350W @8 x 20 us Pulse
- 4. Low Leakage
- 5. Fast Response Time < 5 ns
- 6. Protects One Power or I/O Port
- 7. ESD Protection to IEC 61000-4-2 Level 4,15KV(Air), 8KV(Contact)
- 8. ESD Protection to IEC 61000-4-2 Level 4, 30A
- 9. 16KV Human Body Model ESD Requirements
- 10. RoHS Compliant in Lead-Free Versions

APPLICATION:

- 1. Cell Phone Handsets and Accessories
- 2. Microprocessor Based Equipment
- 3. Personal Digital Assistant (PDA)
- 4. Notebooks, Desktops, and Servers
- 5. Portable Instrumentation
- 6. Pagers Peripherals

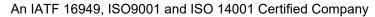
ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

PARAMETER			SYMBOL	VALUE	UNIT
Peak Power Dissipation (Note 1.) @T _L = 25°C			P_{PK}	350	W
IEC 61000-4-2 (ESD)		Air		±15	KV
		CONTACT		±8.0	KV
IEC 61000-4-4 (EFT)				30.0	Α
ESD Voltage	Per Human Body Model		V_{PP}	16.0	KV
Storage Temperature Range			T _{STG}	-55 to +150	°C
Operating Junction Temperature Range			T_J	-55 to +150	°C

Note:

1.8 X 20 us, non-repetitive









ELECTRICAL CHARACTERISTICS at (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL
Maximum Reverse Peak Pulse Current	I _{PP}
Clamping Voltage @ I _{PP}	V_{C}
Working Peak Reverse Voltage	V_{RWM}
Maximum Reverse Leakage Current @ V _{RWM}	I _R
Test Current	I _T
Breakdown Voltage @ I _T	V_{BR}

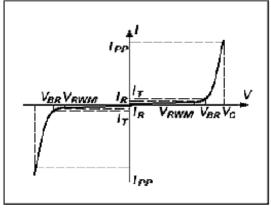


Fig 2: Power Derating

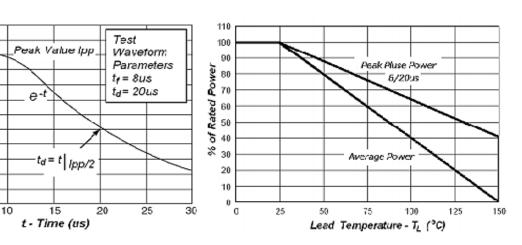
ELECTRICAL CHARACTERISTICS at (Ta = 25 °C Unless otherwise specified)

Device	V _{RWM} (V)	I _R (uA) @ V _{RWM}	V _{BR} (V)@ I	T (Note 1)	Ι _τ	V _C (V) @	VC (V) @ Max I _{PP}	I _{PP} (A) [*]
	Max	Max	Min	Max	mA	Тур	Max	Max
SD03C	3.0	10	4.0	6.1	1	9.0	13.3	20
SD05C	5.0	10	5.0	7.3	1	9.8	14.5	20
SD12C	12	1.0	13.3	15.75	1	19	25	12

^{*} Surge current waveform per Figure 1.

TYPICAL CHARACTERISTICS CURVES

Fig 1: Pulse Waveform



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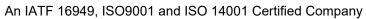
60

40

0

^{1.} V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C



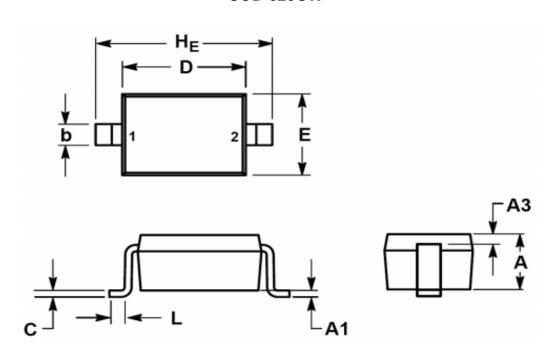






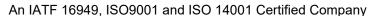
PACKAGE DETAILS

SOD-323GW



DIM	ı	Millimeters		Inches		
DIIVI	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.90	1.00	0.031	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A3	0.15 REF			0.006 REF		
b	0.25	0.32	0.4	0.010	0.012	0.016
С	0.080	0.12	0.177	0.003	0.005	0.007
D	1.60	1.70	1.80	0.063	0.066	0.071
Е	1.15	1.25	1.40	0.045	0.049	0.055
L	0.08			0.003		
H _E	2.3	2.5	2.7	0.090	0.098	0.106







Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- · Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- · Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

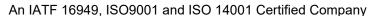
Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level				
Level	Time	Condition		
1	Unlimited	≤30 °C / 85% RH		
2	1 Year	≤30 °C / 60% RH		
2a	4 Weeks	≤30 °C / 60% RH		
3	168 Hours	≤30 °C / 60% RH		
4	72 Hours	≤30 °C / 60% RH		
5	48 Hours	≤30 °C / 60% RH		
5a	24 Hours	≤30 °C / 60% RH		
6	Time on Label(TOL)	≤30 °C / 60% RH		









Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



Continental Device India Pvt. Limited

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