

5.0Amp Schottky Barrier Rectifiers

Reverse Voltage 40 ~ 200 V, Forward Current 5.0 A



SR540~SR5200

DO-201AD
Axial Leaded
Plastic Package
RoHS compliant

FEATURES:

- 1. Low reverse leakage
- 2. High forward surge capability
- 3. High reliability
- 4. High temperature soldering guaranteed 260°C/10seconds
- 5. Lead and body according with RoHS standard
- 6. This product is available in AEC-Q101 Compliant and PPAP Capable also

Note: Foe AEC-Q101 compliant products, please suffix - AQ in the part number while ordering

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

PARAMETER		SYMBOL	SR540	SR560	SR5100	SR5150	SR5200	UNIT
Maximum repetitive peak reverse voltage		V_{RRM}	40	60	100	150	200	V
Maximum RMS voltage		V_{RMS}	28	42	70	105	140	V
Maximum DC blocking voltage		V_{DC}	40	60	100	150	200	V
Maximum average forward rectified current		I _{F(AV)}	5.0			Α		
Non-repetitive peak forward surge current 8.3 ms single half sine-wave		I _{FSM}	150		Α			
Maximum forward voltage @I _F =5.0A		V_{F}	0.55	0.70	0.85	0.92	0.95	V
Maximum reverse current	T _A =25°C	_	0.5		0.1		mA	
iviaximum reverse current	T _A =100°C	· I _R	2	20 10				
Type junction capacitance at V _R =4.0V,f=1MHz		CJ	400			pF		
Typical thermal resistance ¹		$R_{\theta JA}$	20				°C/W	
		$R_{\theta JL}$	10					
Operating junction temperature		T_J	-55 to +125 -55 to +150		°C			
Storage temperature range		T _{STG}	-55 to +150			°C		

Note:

1.Thermal resistance from junction to ambient, PCB mounted.

SR520_5200 Rev3_15022024EFZ





TYPICAL CHARACTERISTIC CURVES

Fig 1: TYPICAL FORWARD CHARACTERISTIC

(V)

10

SR540

SR560

SR5150

SR5150

SR5200

0.1

VF Instantaneous Forward Voltage (V)

0.5

0

2.0

1.5

Tamb, ambient temperature (°C)

Fig 2: MAXIMUM NON REPETITIVE PEAK FORWARD SURGE CURRENT

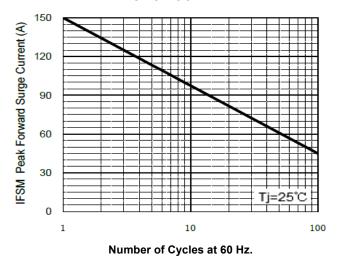
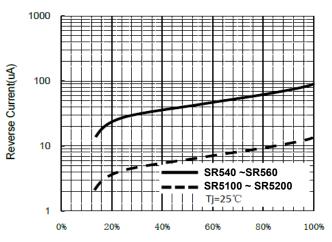


Fig 4: Typical Reverse Characteristics



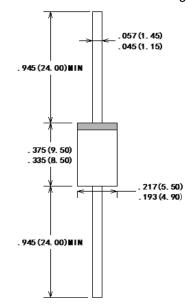
Percent Of Rated Peak Reverse voltage %





Package Details

DO-201AD Axial Plastic Package



All dimensions are in inches(mm)

Mechanical Data

Case: Molded plastic body

Epoxy: UL 94V-0 rate flame retardant

Lead: Pure tin plated, lead free

System: Accreditation through IATF16949 System

High reliability grade (AEC Q101 qualified)

SR520_5200 Rev3_15022024EFZ





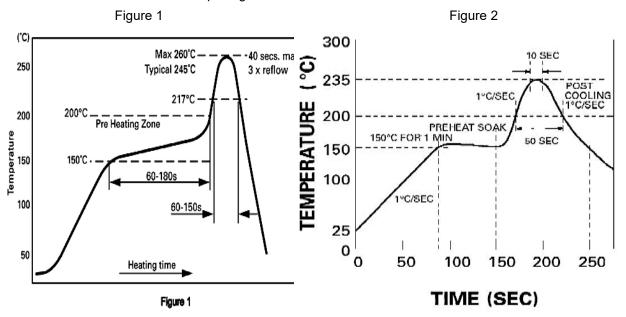


Recommended Reflow Solder Profiles

The recommended reflow solder profiles for Pb and Pb-free devices are shown below.

Figure 1 shows the recommended solder profile for devices that have Pb-free terminal plating, and where a Pb-free solder is used.

Figure 2 shows the recommended solder profile for devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with a leaded solder.



Reflow profiles in tabular form

Profile Feature	Sn-Pb System	Pb-Free System
Average Ramp-Up Rate	~3°C/second	~3°C/second
Preheat - Temperature Range - Time	150-170°C 60-180 seconds	150-200°C 60-180 seconds
Time maintained above: – Temperature – Time	200°C 30-50 seconds	217°C 60-150 seconds
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	40 seconds
Ramp-Down Rate	3°C/second max.	6°C/second max.



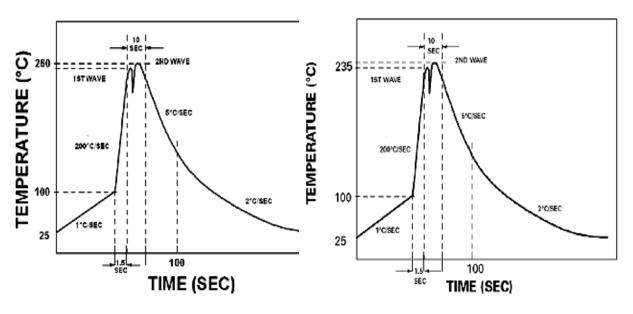




Recommended Wave Solder Profiles

The Recommended solder Profile For Devices with Pb-free terminal plating where a Pb-free solder is used

The Recommended solder Profile For Devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with leaded solder



Wave Profiles in Tabular Form

Profile Feature	Sn-Pb System	Pb-free System		
Average Ramp-Up Rate	~200°C/second	~200°C/second		
Heating rate during preheat	Typical 1-2, Max 4°C/sec	Typical 1-2, Max 4°C/Sec		
Final preheat Temperature	Within 125°C of Solder Temp	Within 125°C of Solder Temp		
Peak Temperature	235°C	260°C max.		
Time within +0 -5°C of actual Peak	10 seconds	10 seconds		
Ramp-Down Rate	5°C/second max.	5°C/second max.		

SR520_5200 Rev3_15022024EFZ



Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- · Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- · Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level				
Level	Time	Condition		
1	Unlimited	≤30 °C / 85% RH		
2	1 Year	≤30 °C / 60% RH		
2a	4 Weeks	≤30 °C / 60% RH		
3	168 Hours	≤30 °C / 60% RH		
4	72 Hours	≤30 °C / 60% RH		
5	48 Hours	≤30 °C / 60% RH		
5a	24 Hours	≤30 °C / 60% RH		
6	Time on Label(TOL)	≤30 °C / 60% RH		

SR520_5200 Rev3_ 15022024EFZ







Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving /support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s). CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered trademark of

Continental Device India Pvt. Limited

C-120 Naraina Industrial Area, New Delhi 110 028, India. Telephone +91-11-2579 6150, 4141 1112 Fax +91-11-2579 5290, 4141 1119 email@cdil.com www.cdil.com CIN No. U32109DL1964PTC004291

SR520 5200 Rev3 15022024EFZ