

SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

Reverse Voltage - 20 to 200 Volts Forward Current - 1.0 Ampere

SS12 ~ SS120

DO-214AC (SMA) Surface Mount Plastic Package

RoHS compliant

SÜD

ATE 1694

NT SYSTEM CER

DNV



DO-214AC (SMA)

FEATURES:

1. The plastic package carries Underwriters Laboratory Flammability Classification 94V-0

- 2. For surface mounted applications
- 3. Metal silicon junction, majority carrier conduction
- 4. Low power loss, high efficiency
- 5. Built-in strain relief, ideal for automated placement
- 6. High forward surge current capability
- 7. High temperature soldering guaranteed:250 C/10 seconds at terminals
- 8. This product is available in AEC-Q101 Compliant and PPAP Capable also.

Note: For AEC-Q101 compliant products, please use suffix -AQ in the part number while ordering.

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	SS1 2	SS 13	SS 14	SS 15	SS 16	SS 18	SS 110	SS 115	SS 120	UNIT
Maximum repetitive peak reverse V _{RRN}		20	30	40	50	60	80	100	150	200	V
Maximum RMS voltage	V _{RMS}	14	21	28	35	42	56	70	105	140	V
Maximum DC blocking voltage	V _{DC}	20	30	40	50	60	80	100	150	200	V
Maximum average forward rectified current at T _L (see fig.1)	I _(AV)	1.0						А			
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	30.0			A						
Maximum instantaneous forward voltage at 1.0A	V _F	0.55 0.70 0.85		85	0.	95	V				
Maximum DC reverse current T _A =25°C		0.2 1		1	mA						
laximum DC reverse current T _A =25°C			5.0 10					ШA			
Typical junction capacitance ¹	¹ C _J 110 90		pF								
pical thermal resistance ² R _{qJA} 88.0			°C/W								
Operating junction temperature range	TJ	-65 to +125 -65 to +150			°C						
Storage temperature range	T _{STG}	-65 to +150			°C						

Note:

1. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

2. P.C.B. mounted with 0.2x0.2"(5.0x5.0mm) copper pad areas

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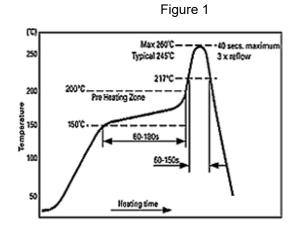


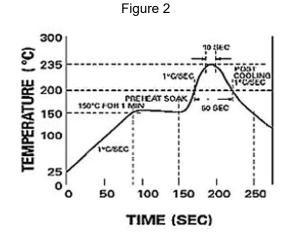
Recommended Reflow Solder Profiles

The recommended reflow solder profiles for Pb and Pb-free devices are shown below.

Figure 1 shows the recommended solder profile for devices that have Pb-free terminal plating, and where a Pb-free solder is used.

Figure 2 shows the recommended solder profile for devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with a leaded solder.





Reflow profiles in tabular form

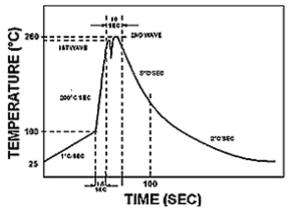
Profile Feature	Sn-Pb System	Pb-Free System
Average Ramp-Up Rate	~3°C/second	~3°C/second
Preheat – Temperature Range – Time	150-170°C 60-180 seconds	150-200°C 60-180 seconds
Time maintained above: – Temperature – Time	200°C 30-50 seconds	217°C 60-150 seconds
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	40 seconds
Ramp-Down Rate	3°C/second max.	6°C/second max.

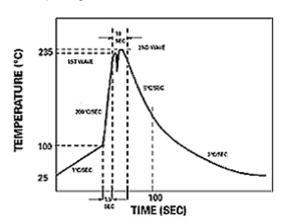




Recommended Wave Solder Profiles

The Recommended solder Profile For Devices with Pb-free terminal plating where a Pb-free solder is used





The Recommended solder Profile For Devices

with Pb-free terminal plating used with leaded

solder, or for devices with leaded terminal

plating used with leaded solder

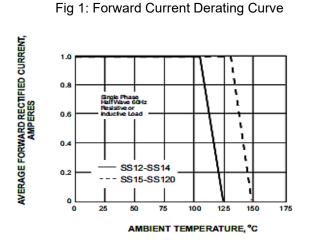
Wave Profiles in Tabular Form

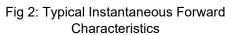
Wave Fromes in Fabular Form						
Profile Feature	Sn-Pb System	Pb-Free System				
Average Ramp-Up Rate	~200°C/second	~200°C/second				
Heating rate during preheat	Typical 1-2, Max 4°C/sec	Typical 1-2, Max 4°C/Sec				
Final preheat Temperature	Within 125°C of Solder Temp	Within 125°C of Solder Temp				
Peak Temperature	235°C	260°C max.				
Time within +0 -5°C of actual Peak	10 seconds	10 seconds				
Ramp-Down Rate	5°C/second max.	5°C/second max				

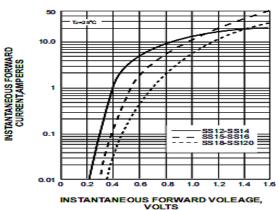


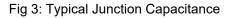


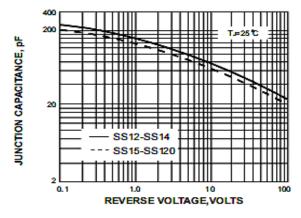
TYPICAL CHARACTERISTICS CURVES











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Fig 4: Maximum Non-Repetitive Peak Forward Surge Current

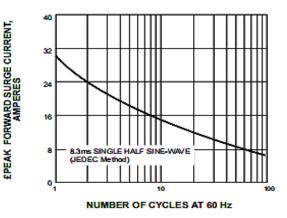


Fig 5: Typical Reverse Characteristics

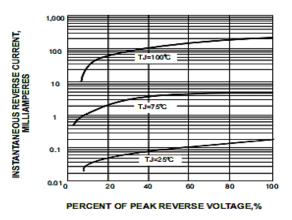
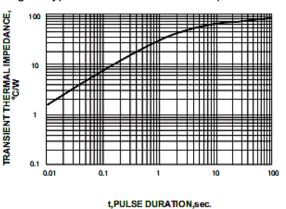


Fig 6: Typical Transient Thermal Impedance



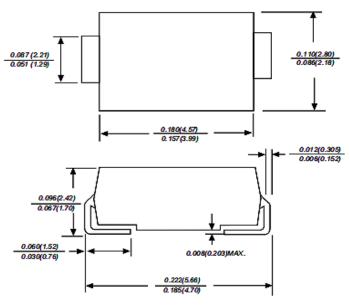
Continental Device India Pvt. Limited





PACKAGE DETAILS

DO-214AC (SMA) Plastic Package

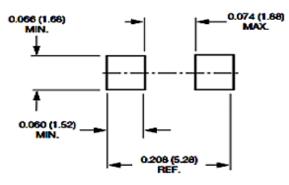


All dimensions are in mm

MECHANICAL DATA

Case: JEDEC DO-214AC molded plastic body Terminals: leads Solderable per MIL-STD-750,Method 2026 Polarity: Color band denotes cathode end Mounting Position: Any Weight: 0.058 grams

SOLDER PAD LAYOUT



Dimensions are in inches(mm)

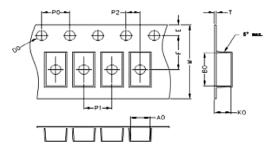
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Package Information

Carrier Dimension (mm)



A0	B0	к0	D0	E	F
2.80	5.30	2.36	1.55	1.75	5.50
P0	P1	P2	т	w	Tolerance
4.0	4.0	2.0	0.25	12	0.1

Packing Specification

Package	Reel Size	Reel DIA. (mm)	Q'TY/Reel (Kpcs)	Box Size (mm)	QTY/Box (Kpcs)	Carton Size (mm)	Q'TY/Carton (Kpcs)
	11'	278	5	285	10	355*310*310	80
SMA	13'	330	7.5	340	15	360*360*360	120





Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- \cdot Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- \cdot Mechanical stress such as vibration and impact shall be avoided.
- \cdot The product shall not be placed directly on the floor.
- \cdot The product shall be stored on a plane area. They should not be turned upside dawn. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start. For this, the following JEDEC table may be referred:

JEDEC MSL Level					
Level	Time	Condition			
1	Unlimited	≤30 °C / 85% RH			
2	1 Year	≤30 °C / 60% RH			
2a	4 Weeks	≤30 °C / 60% RH			
3	168 Hours	≤30 °C / 60% RH			
4	72 Hours	≤30 °C / 60% RH			
5	48 Hours	≤30 °C / 60% RH			
5a	24 Hours	≤30 °C / 60% RH			
6	Time on Label(TOL)	≤30 °C / 60% RH			





Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL wil not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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