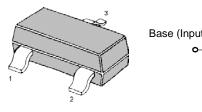




PNP Silicon Epitaxial Planar Transistor



Base (Input)

Collector (Output)

Emitter (Common)

CMBTRA101_106SS SOT-23 Plastic Package

RoHS compliant

1 Base 2 Emitter 3

3. Collector

Features

- 1) With built-in bias resistors
- 2) Simplify circuit design
- 3) Reduce a quantity of parts and manufacturing process

Resistor Values

Туре	R1 (KΩ)	R2 (KΩ)
CMBTRA101SS	4.7	4.7
CMBTRA102SS	10	10
CMBTRA103SS	22	22
CMBTRA104SS	47	47
CMBTRA105SS	2.2	47
CMBTRA106SS	4.7	47

Absolute Maximum Ratings (T_a = 25 °C)

Parameter		Symbol	Value	Unit
Output Voltage		-V _O	50	
Input Voltage	CMBTRA101SS		20, -10	
	CMBTRA102SS	-V ₁	30, -10	V
	CMBTRA103SS		40, -10	
	CMBTRA104SS		40, -10	
	CMBTRA105SS		12, -5	
	CMBTRA106SS		20, -5	
Output Current		-l _o	100	mA
Total Power Dissipation		Ptot	200	mW
Junction Temperature		T _j	150	°C
Storage Temperature Range		Tstg	- 55 to + 150	°C

Note: For AECQ compliant products, please suffix -AH in the part number while ordering





Electrical Characteristics at T_a = 25 °C

Parameter		Symbol Test Conditions		Value		Unit		
		Зуппоот	rest conditions	Min.	Тур.	Max.		
DC	CMBTRA101SS	G _I	at - $V_0 = 5 V$, - $I_0 = 10$ mA	30	-	ı	-	
	CMBTRA102SS			50	-	-	-	
	CMBTRA103SS			70	-	-	-	
Current Gain	CMBTRA104SS			80	-	-	-	
	CMBTRA105SS			80	-	-	-	
	CMBTRA106SS			80	-	-	-	
Output Cutoff Current		-IO(OFF)	at -V _O = 50 V	-	-	500	nA	
	CMBTRA101SS	-I _I	at -V _I = 5 V	-	-	1.8	mA	
	CMBTRA102SS			-	-	0.88		
Input Current	CMBTRA103SS			-	-	0.36		
	CMBTRA104SS			-	-	0.18		
	CMBTRA105SS			-	-	3.6		
	CMBTRA106SS			-	-	1.8		
Output Voltage		-VO(ON)	at $I_0 = 10 \text{ mA}, I_1 = 0.5 \text{ mA}$	-	-	0.3	V	
	CMBTRA101SS	-VI(ON)	at $-V_0 = 0.2 \text{ V}$, $-I_0 = 5$ mA	-	-	2	V	
	CMBTRA102SS			-	-	2.4		
Input Voltage	CMBTRA103SS				•	3		
(ON)	CMBTRA104SS				•	5		
	CMBTRA105SS			-	-	1.1		
	CMBTRA106SS			-	-	1.3		
Input	CMBTRA101SS~104SS	\//\OFF\	\/I/OFF\	at $-V_0 = 5 \text{ V}$, $-I_0 = 0.1$	1	-	-	V
Voltage (OFF)	CMBTRC105SS~106SS	-VI(OFF)	mA	0.5	-		V	
Transition	Frequency	fT ¹⁾	at $-V_0 = 10 \text{ V}, -I_0 = 5$ mA	-	200	-	MHz	

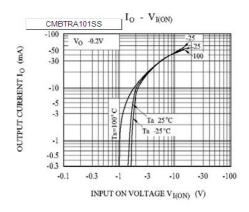
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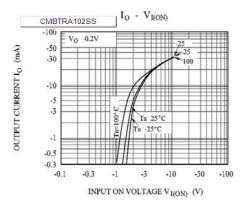
¹⁾ Characteristic of transistor only

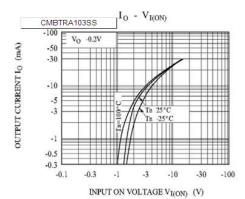


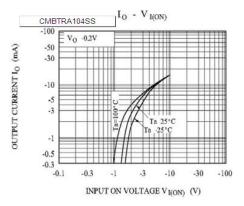


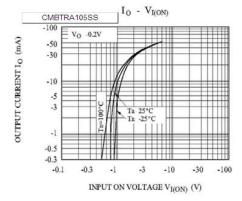
Typical Characteristic curves

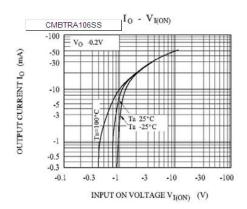










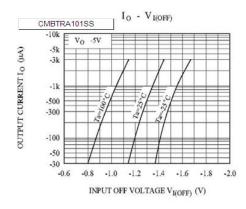


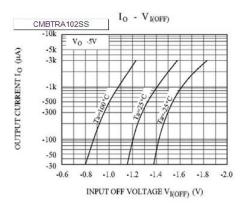


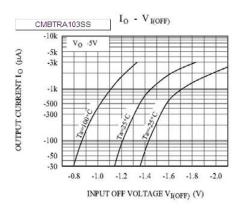


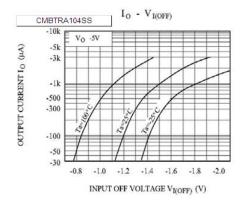


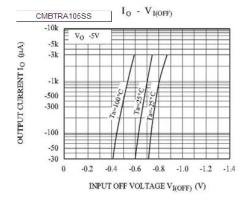
Typical Characteristic curves (continued_____

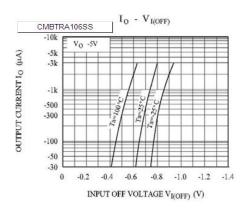










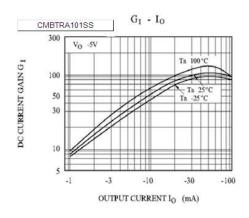


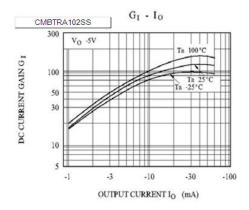


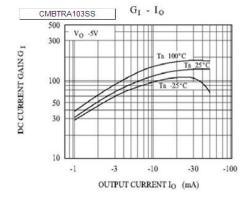


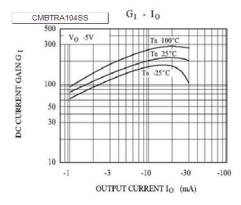


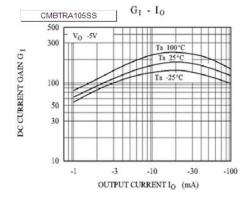
Typical Characteristic curves (continued_____

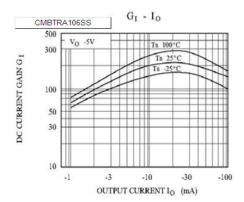








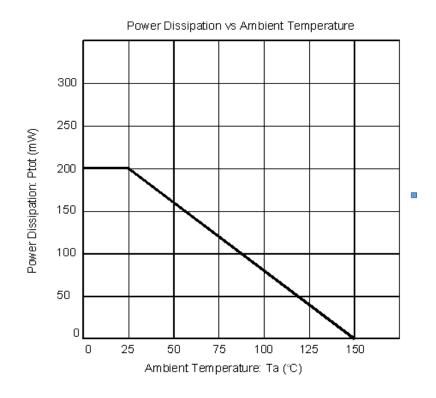


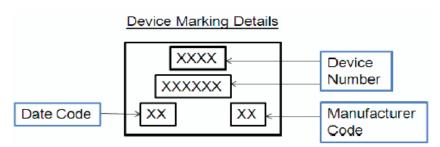






Typical Characteristic curves (continued______









Recommended Product Storage Environment for Diode and Transistors

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- · Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying.
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years, the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start. For this the following JEDEC table may be referred:

JEDEC MSL Level			
Level	Time	Condition	
1	Unlimited	≤30 °C / 85% RH	
2	1 Year	≤30 °C / 60% RH	
2a	4 Weeks	≤30 °C / 60% RH	
3	168 Hours	≤30 °C / 60% RH	
4	72 Hours	<30 °C / 60% RH	
5	48 Hours	≤30 °C / 60% RH	
5a	24 Hours	≤30 °C / 60% RH	
6	Time on Label(TOL)	≤30 °C / 60% RH	

Figure 1 Floor Life according to JEDEC MSL Level





Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use I life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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