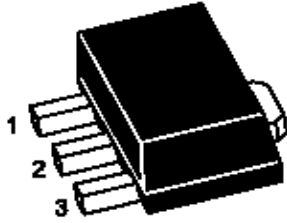


PNP SILICON EPITAXIAL PLANAR TRANSISTOR

BCX53.16



1.Base 2.Collector 3.Emitter

SOT-89

Surface Mount

Plastic Package

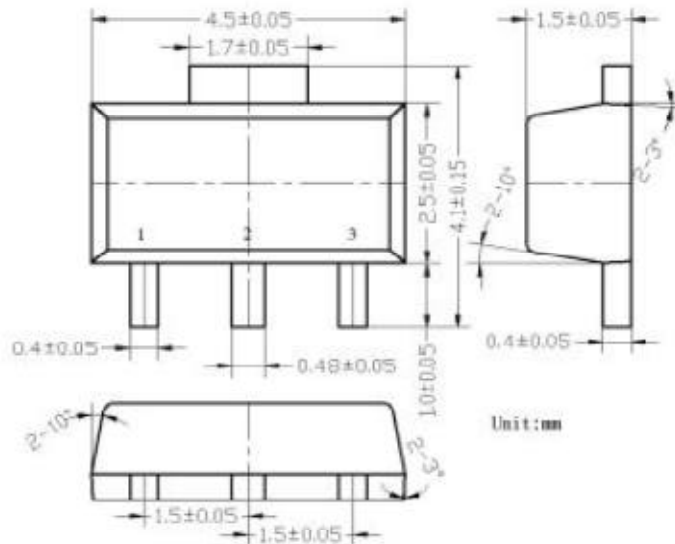
ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$)

DESCRIPTION	SYMBOL	VALUE	UNITS
Collector Base Voltage	V_{CBO}	100	V
Collector Emitter Voltage	V_{CEO}	80	V
Emitter Base Voltage	V_{EBO}	5	V
Collector Current Continuous	I_C	1	A
Total Power Dissipation at $T_c=25^\circ\text{C}$	P_{tot}	1	W
Junction Temperature	T_j	150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to +150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$ unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C=10\text{mA}$, $I_B=0$	80			V
Collector Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C=100\mu\text{A}$	100			V
Emitter Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E=100\mu\text{A}$	5			V
Collector Base Cut Off Current	I_{CBO}	$V_{CB}=30\text{V}$			100	nA
Emitter Base Cut Off Current	I_{EBO}	$V_{EB}=5\text{V}$			100	nA
DC Current Gain	h_{FE}	$I_C=5\text{mA}$, $V_{CE}=2\text{V}$	25			
		$I_C=150\text{mA}$, $V_{CE}=2\text{V}$	100		250	
		$I_C=500\text{mA}$, $V_{CE}=2\text{V}$	25			
Collector Emitter Saturation Voltage	$V_{CE(Sat)}$	$I_C=500\text{mA}$, $I_B=50\text{mA}$			0.5	V
Base Emitter On Voltage	$V_{BE(on)}$	$I_C=500\text{mA}$, $V_{CE}=2\text{V}$			1	V
Transition Frequency	f_T	$V_{CE}=5\text{V}$, $I_C=10\text{mA}$		50		MHz

SOT-89 PACKAGE DIMENSION



1 . BASE

2. COLLECTOR

3. EMITTER



Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company



Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).



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Continental Device India Limited

C-120 Naraina Industrial Area, New Delhi 110 028, India.

Telephone + 91-11-2579 6150, 4141 1112 Fax + 91-11-2579 5290, 4141 1119

e-mail sales@cdil.com www.cdil.com

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