





NPN SILICON EPITAXIAL POWER TRANSISTORS

C44C8 C44C11



TO-220

TO-220 Leaded Plastic Package RoHS compliant

APPLICATION: Medium Power Switching and Amplifier Applications

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

DADAM	SYMBOL	VALUE		LINUT		
PARAMETER		STWIBUL	C44C8	C44C11	UNIT	
Collector- Emitter Voltage		V_{CES}	70	90	V	
Collector- Emitter Voltage		V_{CEO}	60	80	V	
Emitter- Base Voltage		V_{EBO}	5		V	
Collector Current	Continuous	I _C	4		^	
Continuous	Peak ¹	I _{CM}	6		A	
Base Current Continuous		I _B	2		Α	
Dower Dissipation	T _A =25°C	Б	1.67		۱۸/	
Power Dissipation	T _C =25°C	P_{D}	30		W	
Operating & Storage Junction Temperature Range		T_{j},T_{stg}	-55 to +150		°C	

Thermal Resistance

Junction to Ambient	R _{th (j-a)}	75	°C/W
Junction to Case	R _{th (i-c)}	4.2	°C/W







ELECTRICAL CHARACTERISTICS at (Ta = 25 °C Unless otherwise specified)

PARAMETER		SYMBOL TEST CONDITION		VALUE			LINIT
		STIVIBUL	TEST CONDITION	MIN	TYP	MAX	UNIT
Collector- Emitter	ollector- Emitter C44C8		\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	60		-	V
Suntanning Voltage	C44C11	V _{CEO(sus)} ¹	I _C =100mA, I _B =0	80		-	V
Collector Cut Off Current		I _{CES}	V_{CE} =Rated V_{CES}	-		10	μA
Emitter Cut Off Current		I _{EBO}	$V_{EB}=5V, I_{C}=0$	-		100	μA
DO Comment Cain		L 1	I_{C} =0.2A, V_{CE} =1V	100		220	
DC Current Gain		h _{FE} 1	$I_C=2A$, $V_{CE}=1V$	20		-	
Collector Emitter Saturation Voltage		$V_{\text{CE(sat)}}^{1}$	$I_C=1A$, $I_B=50mA$			0.5	V
Base Emitter Saturation Voltage		V _{BE(sat)} 1	$I_C=1A$, $I_B=100$ mA			1.3	V
Dynamic Characteristics							
Collector Capacitance		C_cbo	V_{CB} =10V, I_{E} =0 f=1MHz			100	pF
Current Gain Bandwidth Product		f_T	V_{CE} =4V, I_{C} =20mA		50		MHz
Switching Time							
Delay Time + Rise Time		$t_d + t_r$	$I_C=1A$, $I_{B1}=1_{B2}=0.1A$		100		ns
Storage Time		t_s	\/ -20\/ t -25uc		500		no
Fall Time		t _f	V_{CC} =30V, t_p =25 μ s		75		ns

Note:

1. Pulse Test Pulse Width≤300µs, Duty Cycle≤2%

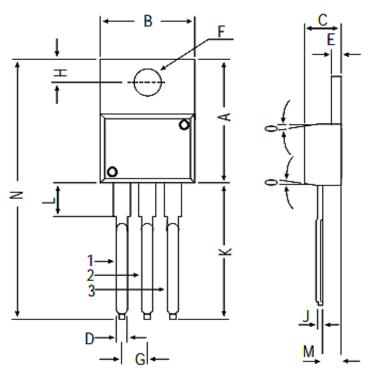






PACKAGE DETAILS

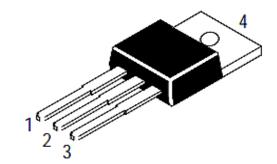
TO-220 Plastic Package



DIM	MIN	MAX
Α	14.42	16.51
В	9.63	10.67
С	3.56	4.83
D		0.90
Е	1.15	1.40
F	3.75	3.88
G	2.29	2.79
Н	2.54	3.43
J		0.56
K	12.70	14.73
L	2.80	4.07
М	2.03	2.92
N		31.24
0	7	70

PIN CONFIGURATION

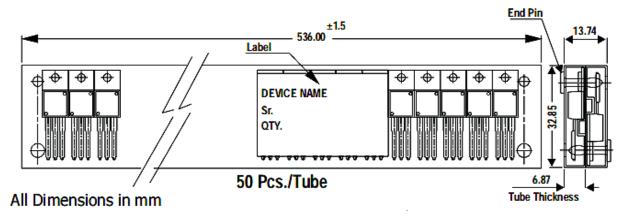
- 1. BASE
- 2. COLLECTOR
- 3. EMITTER
- 4. COLLECTOR



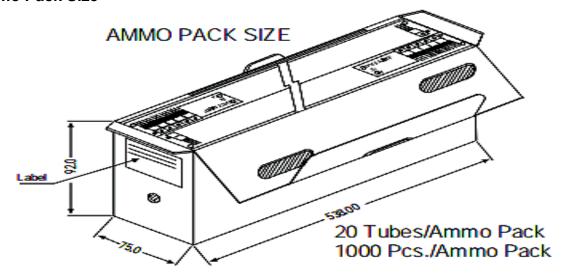




TO-220 Tube Packing



Ammo Pack Size



Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net:Weight/Qty	Size	Qty	Size	Qty	G-MF
TO-220/FP	200 pcs/pdlybag	396 gm/200 pcs	3"x7.5"x7.5"	1.0K	17"×15"×13.5"	16.0K	36 kgs
	50 pcs/tube	120 gm/50 pcs	3.5'x3.7'x21.5'	1.0K	19"x19"x19"	10.0K	29 kgs





Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- · Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- · Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level					
Level	Time	Condition			
1	Unlimited	≤30 °C / 85% RH			
2	1 Year	≤30 °C / 60% RH			
2a	4 Weeks	≤30 °C / 60% RH			
3	168 Hours	≤30 °C / 60% RH			
4	72 Hours	≤30 °C / 60% RH			
5	48 Hours	≤30 °C / 60% RH			
5a	24 Hours	≤30 °C / 60% RH			
6	Time on Label(TOL)	≤30 °C / 60% RH			







Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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