



Silicon PNP POWER Transistors

CSA1006,CSA1006B

TO-220 Leadded Plastic Package RoHS compliant

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TO-220

Applications:

- 1. Audio frequency power amplifier
- 2. High frequency power amplifier

ABSOLUTE MAXIMUM RATINGS ($T_a = 25 \circ C$)

Parameter			Symbol	Value	Unit	
Collector-Base Voltage CSA1006 CSA1006E			V	-180	V	
			V _{CBO}	-250		
Collector Emitter Voltage CSA1006			V	-180	V	
Collector-Emitter Voltage CSA100		CSA1006B	V _{CEO}	-250	V	
Emitter-Base	Voltage	V_{EBO}	-5	V		
Collector Cur	rent	I _C	-1.5	Α		
Collector Cur	rentPeak	I _{CM}	-3	Α		
Total Dissipa	tion	P _{tot}	25	W		
Max. Operati	ng Junction Ten	Tj	150	°C		
Storage Tem	perature	T _{stg}	-55~150	°C		





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Dememorten		Symbol	Test Osditions	Value			11
Parameter	Test Coditions		Min.	Тур.	Max.	Unit	
Collector Cut-off Current	CSA1006	I	V _{CB} = 180V, I _E = 0	-		-1	uA
	CSA1006B	I _{CBO}	V _{CB} = -250V, I _E = 0			-1	uA
Emitter Out off Ourrent	CSA1006	I _{EBO}	V _{EB} = 5.0V, I _C = 0			-1	uA
Emitter Cut-off Current	CSA1006B		V _{EB} = -5.0V, I _C = 0			-1	uA
Collector-Emitter Sustaining	CSA1006	N/	IC = 10mA, IB = 0	-180			V
Voltage	CSA1006B	V_{CEO}	IC = IUIIA, IB = 0	-250			V
	CSA1006	. (1)	V _{CE} = 5.0V,	60		320	
	CSA1006B	$h_{FE}^{(1)}$	I _c = 150mA				
DC Current Gain	CSA1006	. (2)	V _{CE} =5.0V,				
	CSA1006B	$h_{FE}^{(2)}$	I _c = 5.0mA	30			
Collector-Emitter Saturation	CSA1006		I = 500 m A $I = 50 m A$			-1	V
Voltage	CSA1006B	V _{CE(sat)}	I _C = 500mA, I _B = 50mA				
Base-Emitter saturation	CSA1006		I _C = 500mA,			-1.5	V
Voltage	CSA1006B	V _{BE(sat)}	I _B = 50mA				
Current Gain Bandwidth	CSA1006		V _{CE} = 10V,				MHz
Product	CSA1006B	f _⊤	$I_c = 100 \text{mA}$		80		

ELECTRICAL CHARACTERISTICS at T_a = 25 °C





Continental Device India Pvt. Limited An IATF 16949, ISO9001 and ISO 14001 Certified Company Typical Characteristic curves

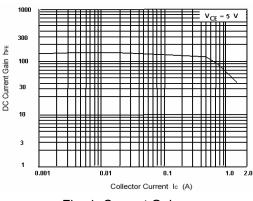
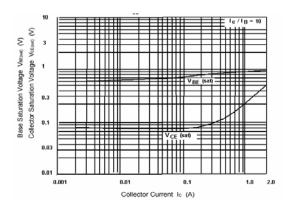
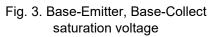


Fig. 1. Current Gain





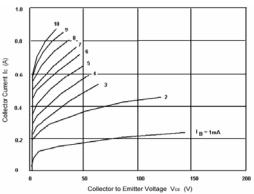
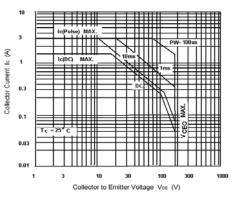


Fig. 2. Stataic Characteristic

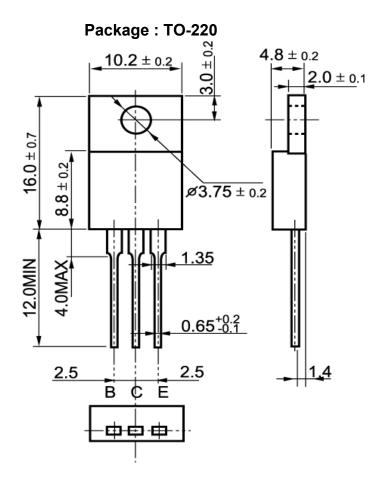








Package Details



All dimensions are in mm

Note: For AECQ compliant products, please suffix -AH in the part number while ordering





<u>Recommended Product Storage Environment for Diode and</u> <u>Transistors</u>

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying.
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years, the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start. For this the following JEDEC table may be referred:

JEDEC MSL Level						
Level	Time	Condition				
1	Unlimited	≤30 °C / 85% RH				
2	1 Year	<u>≤</u> 30 °C / 60% RH				
2a	4 Weeks	<u><</u> 30 °C / 60% RH				
3	168 Hours	≤30 °C / 60% RH				
4	72 Hours	≤30 °C / 60% RH				
5	48 Hours	<u>≤</u> 30 °C / 60% RH				
5a	24 Hours	<u>≤</u> 30 °C / 60% RH				
6	Time on Label(TOL)	<u>≤</u> 30 °C / 60% RH				

Figure 1 Floor Life according to JEDEC MSL Level

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Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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