



SURFACE MOUNT SUPER FAST RECTIFIER

ES5A-ES5J



DO-214AB(SMC) Plastic Package RoHS compliant

DO-214AB (SMC)

FEATURES:

- 1. The plastic package carries Underwriters Laboratory
- 2. Flammability Classification 94V-0
- 3. For surface mounted applications
- 4. Low reverse leakage
- 5. Built-in strain relief, ideal for automated placement
- 6. High forward surge current capability
- 7. High temperature soldering guaranteed:250 C/10 seconds at terminals
- 8. Glass passivated chip junction

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	ES5A	ES5B	ES5C	ES5D	ES5E	ES5G	ES5J	UNIT
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	150	200	300	400	600	V
Maximum RMS voltage	V _{RMS}	35	70	105	140	210	280	420	V
Maximum DC blocking voltage	V _{DC}	50	100	150	200	300	400	600	V
Maximum average forward rectified current at T _L =75°C	I _(AV)	5.0						А	
Peak forward surge current 8.3ms single half sine-wave Superimposed on rated load (JEDEC Method)	I _{FSM}	150.0					А		
Maximum instantaneous forward voltage at 5.0A	V_{F}	0.95 1.3		.3	1.7	V			
Maximum DC reverse current T _A =25 °C		10.0							- uA
at rated DC blocking voltage T _A =100 °C	I _R	300.0							
Maximum reverse recovery time (NOTE 1)	T _{rr}	35				ns			
Typical junction capacitance (NOTE 2)	CJ	58.0				рF			
Typical thermal resistance	R _{qJA}	47.0			°C/W				
Operating junction and storage temperature range	T _J ,T _{STG}	-65 to +150				°C			

Note:

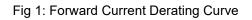
1. Reverse recovery condition $I_F=0.5A$, $I_R=1.0A$, $I_{rr}=0.25A$

2. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

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TYPICAL CHARACTERISTICS CURVES



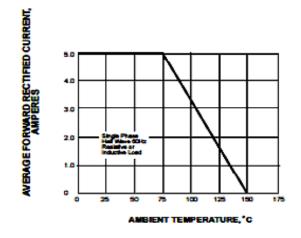


Fig 3: Typical Instantaneous Forward Characteristics

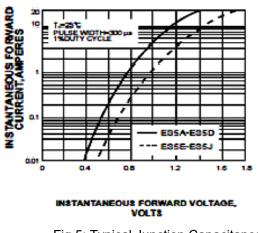
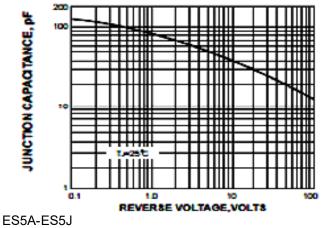


Fig 5: Typical Junction Capacitance



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Surge Current

Fig 2: Maximum Non-Repetitive Peak Forward

Fig 4: Typical Reverse Characteristics

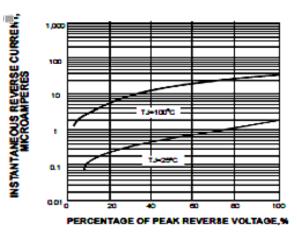
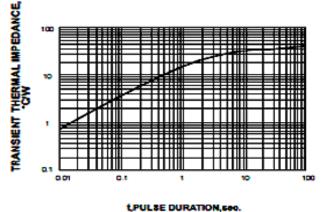
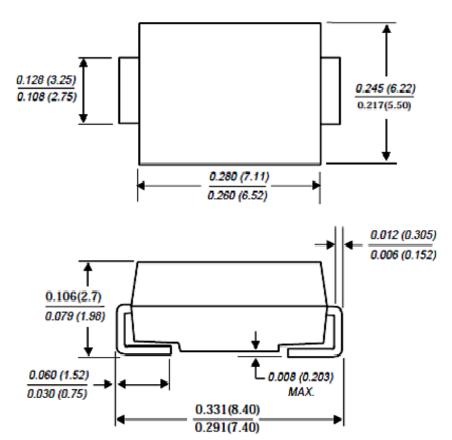


Fig 6: Typical Transient Thermal Impedance





PACKAGE DETAILS



DO-214AB (SMC) Surface Moumt Plastic Package

All dimensions are in inches(mm)

MECHANICAL DATA

- 1. Case: JEDEC DO-214AB molded plastic body over passivated chip
- 2. Terminals: Solder plated, solderable per MIL-STD-750,Method 2026
- 3. Polarity: Color band denotes cathode end
- 4. Mounting Position: Any





Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- $\cdot\,$ Air should be clean.
- · Avoid harmful gas or dust.
- \cdot Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- \cdot Mechanical stress such as vibration and impact shall be avoided.
- $\cdot\,$ The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start. For this, the following JEDEC table may be referred:

JEDEC MSL Level					
Level	Time	Condition			
1	Unlimited	≤30 °C / 85% RH			
2	1 Year	≤30 °C / 60% RH			
2a	4 Weeks	≤30 °C / 60% RH			
3	168 Hours	≤30 °C / 60% RH			
4	72 Hours	≤30 °C / 60% RH			
5	48 Hours	≤30 °C / 60% RH			
5a	24 Hours	≤30 °C / 60% RH			
6	Time on Label(TOL)	≤30 °C / 60% RH			





Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving /support appliances or systems. CDIL customers selling these products (either as individua Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s). CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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